

K. Memory (Design & Process Technology) 분과

2020년 2월 14일(금), 10:45-12:15 / Room G (스페이드 II+III, 6층)

■ [FG2-K] Devices for Neuromorphic Computing II

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FG2-K-1 10:45-11:15	[초청] Neural Networks with Memristor Crossbar Network YeonJoo Jeong <i>KIST</i>
FG2-K-2 11:15-11:30	Performance Improvement of InGaZnO-based RRAM with Al₂O₃ Inserting Tunneling Barrier Layer Jingyu Park, Jun Tae Jang, Geumho Ahn, Jungi Min, Sung-Jin Choi, Dong Myong Kim, and Dae Hwan Kim <i>School of Electrical Engineering, Kookmin University</i>
FG2-K-3 11:30-11:45	Multilevel Resistive Switching Characteristics in Bioinspired Solid Polymer Electrolyte Chitosan-based Memristors Shin-yi Min and Won-Ju Cho <i>Department of Electronic Materials Engineering, Kwangwoon University</i>
FG2-K-4 11:45-12:00	Variation Effect on Stateful Logic Gates and Practical Memristive System Young Seok Kim, Myeong Won Son, Hanchan Song, Juseong Park, Jangho An, Jae Bum Jeon, Geun Young Kim, Seoil Son, and Kyung Min Kim <i>Department of Materials Science and Engineering, KAIST</i>
FG2-K-5 12:00-12:15	Introduction of New APBM Stateful Logics based on Two Antiparallel Bipolar Memristors Nuo Xu ^{1,2} , Tae Gyun Park ² , Hae Jin Kim ² , Xinglong Shao ² , Kyung Jean Yoon ² , Tae Hyung Park ² , Liang Fang ¹ , Kyung Min Kim ³ , and Cheol Seong Hwang ² ¹ National University of Defense Technology, ² Seoul National University, ³ KAIST