

## F. Silicon and Group-IV Devices and Integration Technology 분과

2020년 2월 14일(금), 10:45-12:30 / Room B (에메랄드 II+III, 5층)

### ■ [FB2-F] Neuromorphic Technology

좌장: 김경록 교수 (UNIST), 최신현 교수 (KAIST)

<p><b>FB2-F-1</b> 10:45-11:00</p>	<p><b>Classification Methods Using Additional Output Neurons to Increase Inference Accuracy in Hardware-based Binarized Neural Network</b> Hyeongsu Kim, Sung-Tae Lee, Dongseok Kwon, Byung-Gook Park, and Jong-Ho Lee <i>Department of Electrical and Computer Engineering and Inter-University Semiconductor Research Center, Seoul National University</i></p>
<p><b>FB2-F-2</b> 11:00-11:15</p>	<p><b>Improved Neuron Circuit Using Ni/SiN<sub>x</sub>/n<sup>+</sup>-Si RRAM as Synaptic Devices</b> Yeonwoo Kim, Chae Soo Kim, Myung-Hyun Baek, and Byung-Gook Park <i>Inter-University Semiconductor Research Center (ISRC) and Department of Electrical and Computer Engineering, Seoul National University</i></p>
<p><b>FB2-F-3</b> 11:15-11:30</p>	<p><b>Novel NOR Type Synapse Array Using Additional N-well for Weight Update Method</b> Jonghyuk Park, Myung-Hyun Baek, Suhyeon Kim, Young Suh Song, and Byung-Gook Park <i>Department of Electrical and Computer Engineering, Seoul National University</i></p>
<p><b>FB2-F-4</b> 11:30-11:45</p>	<p><b>Investigation on Extremely-thin-body Polysilicon-based Synaptic Transistor</b> Junsu Yu, Myung-Hyun Baek, Kyung Kyu Min, Kyungchul Park, Young Suh Song, and Byung-Gook Park <i>Inter-University Semiconductor Research Center (ISRC) and Department of Electrical and Computer Engineering, Seoul National University</i></p>
<p><b>FB2-F-5</b> 11:45-12:00</p>	<p><b>Relationship Between Threshold Voltage and Membrane Capacitance of Integrate and Fire Neuron in SNN System</b> Gyuhoo Yeom, Dongseok Kwon, Min Kyu Park, and Jong-Ho Lee <i>Department of Electrical and Computer Engineering and Inter-University Semiconductor Research Center, Seoul National University</i></p>
<p><b>FB2-F-6</b> 12:00-12:15</p>	<p><b>Non-linearity Effect of Current Mirror due to High Fan-in on Spiking Neural Network</b> Bosung Jeon, Sungmin Hwang, Kyungchul Park, Jong-Ho Lee, and Byung-Gook Park <i>Department of Electrical and Computer Engineering, Seoul National University</i></p>
<p><b>FB2-F-7</b> 12:15-12:30</p>	<p><b>Effect of Weight Loss of Synaptic Devices on Inference Accuracy</b> Ho-Nam Yoo, Hyeong-Su Kim, and Jong-Ho Lee <i>Inter-University Semiconductor Research Center (ISRC) and Department of Electrical and Computer Engineering (ECE), Seoul National University</i></p>