O. System LSI Design 분과

2020년 2월 14일(금), 15:45-17:30 / Room A (에메랄드 I, 5층)

[FA3-O] VLSI System Design and Application

좌장: 김영민 교수 (홍익대학교), 김지훈 교수 (이화여자대학교)

FA3-O-1 **15:45-16:00**	**Under 1μV/sqrt(Hz) Ultra Low Noise Analog Amplifier for Sensor Systems** Yeun-Jin Choi, Sung-Jun Jo, Dong-Gyu Kim, and Kang-Yoon Lee *Sungkyunkwan University*
FA3-O-2 **16:00-16:15**	**A Design of 5.8GHz DSRC Transceiver Analog Baseband with ASK Demodulator** Mu-Geun Shin, Sung-Jun JO, Sung-Jin Kim, and Kang Yoon Lee *Department of Electrical and Computer Engineering, Sungkyunkwan University*
FA3-O-3 **16:15-16:30**	**6- µA Quiescent Current and Low Inrush Current Applied Pre-charging Method on-chip LDO for Ultra Low Power RX IoT Circuit** Yong Deok Ahn, Su Jin Oh, Sung Jin Kim, and Kang Yoon Lee *Department of Electrical and Computer Engineering, Sungkyunkwan University*
FA3-O-4 **16:30-16:45**	**Implementation on True Random Number Generator (TRNG) Using CMOS Process for Security of IoT Applications** Kang-Un Choi, Gi-Beom Son, and Jong-Phil Hong *Department of Electronic Engineering, Chungbuk National University*
FA3-O-5 **16:45-17:00**	**High Speed HIGHT Block Cipher Hardware Design** Byungjun Choi, Bohun Kim, Junghoon Cho, and Jongsun Park *Department of Electronic Engineering, Korea University*
FA3-O-6 **17:00-17:15**	**Broadband Bandwidth LNA for TVWS** Young-Uk Kim, Dong-Gyu Kim, Sung-jin Kim, and Kang-Yoon Lee *College of Information and Communication Engineering, Sungkyunkwan University*
FA3-O-7 **17:15-17:30**	**Dead Time Controller in 3-ch DC-DC Converter for AMOLED Display** 김태운, 김찬유, 최호용 *Department of Semiconductor Engineering, Chungbuk National University*