## O. System LSI Design 분과

2020년 2월 14일(금), 10:45-12:30 / Room A (에메랄드 I, 5층)

### [FA2-O] Artificial Intelligent Circuits and Systems

**좌장:** 김영민 교수 (홍익대학교), 김지훈 교수 (이화여자대학교)

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| **FA2-O-1**<br>**10:45-11:00** | **인공 신경망 기반 고성능 LDPC 복호화 기법**<br>최정원, 이영주<br>*POSTECH 전자전기공학과* |
| **FA2-O-2**<br>**11:00-11:15** | **Design of an Always-on Computer Vision Sensor for Face Recognition**<br>Jaihyuk Choi, Minkyu Song, and Soo Youn Kim<br>*Department of Semiconductor Science, Dongguk University* |
| **FA2-O-3**<br>**11:15-11:30** | **Analysis of the Effect of Pruning on Convolutional Neural Network**<br>Dohyun Kim, Yeong-kyo Kim, and Shiho Kim<br>*School of Integrated Technology, Yonsei University* |
| **FA2-O-4**<br>**11:30-11:45** | **Self-timed Spiking Neural Network Chip Design with Efficient Spike Delay Control**<br>JungYeon Lee, Daehu Park, Malik Summair Asghar, JiUn Hong, and HyungWon kim<br>*Department of Electronic Engineering, Chungbuk National University* |
| **FA2-O-5**<br>**11:45-12:00** | **TS-EFA: Resource-efficient High-precision Approximation of Exponential Functions based on Template-scaling Method**<br>Jeeson Kim, Vladimir Kornijcuk, and Doo Seok Jeong<br>*Division of Materials Science and Engineering, Hanyang University* |
| **FA2-O-6**<br>**12:00-12:15** | **강화학습을 위한 이진화된 컨벌루션 신경망 가속 프로세서**<br>최경찬, 박윤성, 김태환<br>*한국항공대학교 항공전자정보공학부* |
| **FA2-O-7**<br>**12:15-12:30** | **High Speed Convolutional Neural Network Architecture with Convolution Accelerator based on Massive Parallel Memory Access**<br>Hyun-Wook Son, Dong-Yeong Lee, Mohammed E. Elbtity, and HyungWon Kim<br>*Department of Electronics, Chungbuk National University* |