M. RF and Wireless Design 분과 [TJ2-M] RF Design II

TJ2-M-1 10:45~11:00	BLE Receiver Employing New Quadrature LNA for IoT Application Beomyu Park and Kuduck Kwon Department of Electronic Engineering, Kangwon National University
TJ2-M-2 11:00~11:15	CMOS Tunable High-Q Channel-Selection Low-Noise Amplifier Employing Frequency-Translated Poly-Phase Filter Donggu Lee and Kuduck Kwon Department of Electronic Engineering, Kangwon National University
TJ2-M-3 11:15~11:30	A +19.3-dBm OIP3 5G mm-Wave down-mixer with LO buffer in 65-nm CMOS technology Yangji Jeon, Seungjik Lee, and Ilku Nam Department of Electrical Engineering, Pusan National University
TJ2-M-4 11:30~11:45	A Design of a Low-Noise RSSI System with Adjustable 20dB Sensitivity Dal-Ho Lee, Hyun-Jae Lee, Sung-Jin Kim, and Kang-Yoon Lee Department of Electrical and Computer Engineering, Sungkyunkwan University
TJ2-M-5 11:45~12:00	35 W 3.4 – 3.8 GHz GaN HEMT 2-Stage Asymmetric Doherty Power Amplifier MMIC for 5G NR Woojin Choi, Hyunuk Kang, and Youngoo Yang Department of Electrical and Computer Engineering, Sungkyunkwan University
TJ2-M-6 12:00~12:15	3-5GHz GaAs p-HEMT Linear Broadband Amplifier for 5G Sub-6 GHz Applications with Capacitive Neutralization Yifei Chen, Sungjae Oh, Wooseok Lee, and Youngoo Yang Department of Electrical and Computer Engineering, Sungkyunkwan University
TJ2-M-7 12:15~12:30	High-Power and High-Efficiency 5.8 GHz GaN-HEMT Rectifier Using Time Reversal Duality for Microwave Power Transfer via Solar Power Satellites Hyungmo Koo, Jongseok Bae, and Youngoo Yang Sungkyunkwan University