S. Chip Design Contest 분과
[TI1-S] Selected Papers on Chip Design Contest

TI1-S-1 09:00~09:15	**A Synchronous Buck Converter Using a Voltage Buffer Compensator** Jun Tang and Jeongjin Roh *Department of Electronics and Communications Engineering, Hanyang University*
TI1-S-2 09:15~09:30	**Monolithic Three-dimensional (M3D) CMOS- Nanoelectromechanical (NEM) Single-tile Reconfigurable Logic (RL)** Hyug Su Kwon and Woo Young Choi *Department of Electronic Engineering, Sogang University*
TI1-S-3 09:30~09:45	**Ultra-small IoT Gas Sensor for Sensing Hazardous Gas** Seungjun Lee[1], Sein Oh[1], Younggyun Oh[1], Juyung Lee[1], Kihyun Kim[1], Joohwan Jin[1], and Hyung Il Chae[2] *[1]Department of Electronic Engineering, Kookmin University, [2]Department of Electronic Engineering, Konkuk University*
TI1-S-4 09:45~10:00	**Implementation of Low-Complexity Extended CCA Hardware Accelerator for Wearable Brain-Computer Interface SoC** Dokyun Kim[1], Wooseok Byun[2], Sung Yeon Kim[1], Hyunji Kim[3], Sunyoung Park[3], and Ji-Hoon Kim[3] *[1]SEOULTECH, [2]Chungnam National University, [3]Ewha Womans University*
TI1-S-5 10:00~10:15	**Low Noise, Low Power 5-Channel Sonar Signal Conditioning Receiver with 1.5 MS/s – 12.5 MS/s 16-bit Sigma-Delta ADC for Ocean Acoustic Measurements** Sang-Gyu Jeon, Tae-Young Yoon, Byeong-Gi Jang, and Kang-Yoon Lee *Sungkyunkwan University*
TI1-S-6 10:15~10:30	**용량형 센서용 저잡음 16 비트 2차 델타-시그마 커패시턴스-디지털 컨버터** 김형섭, 김재성, 한권상, 유동근, 허현우, 권용수, 고형호 *충남대학교 전자공학과*