

2020년 2월 13일(목), 10:45~12:30

Room B (에메랄드 II+III, 5층)

F. Silicon and Group-IV Devices and Integration Technology 분과
[TB2-F] New Applications of Silicon Technology

TB2-F-1 10:45~11:15	[초청] New Generation of SOI Substrate Solutions for RF and Millimeter-wave Applications Jean-Pierre Raskin <i>Université catholique de Louvain</i>
TB2-F-2 11:15~11:45	[초청] Challenges and Opportunities in Memristor-based Artificial Neural Network (ANN) Hardware Shinhyun Choi <i>KAIST</i>
TB2-F-3 11:45~12:00	Schottky Diode in 45-nm Embedded Flash Process for Power Management System YongSeok Chung, ChangHyun Park, Changmin Jeon, Youngho Kim, Sangjin Lee, Yong Kyu Lee, Kichul Park, and Gitae Jeong <i>Foundry Business, Samsung Electronics Co., Ltd.</i>
TB2-F-4 12:00~12:15	Ternary CMOS Technology based on 28-nm Foundry Process Jae Won Jeong, Young Eun Choi, Woo-Seok Kim, and Kyung Rok Kim <i>School of Electrical and Computer Engineering, UNIST</i>
TB2-F-5 12:15~12:30	Proposal and Simulation of a Low Loss, Highly Efficient Monolithic III-V/Si Optical Phase Shifter SangHyeon Kim ^{1,2} , Younghyun Kim ² , Yoojin Ban ² , Pantouvaki Marianna ² , and Joris Van Campenhout ² ¹ <i>School of Electrical Engineering, KAIST</i> , ^{2,3} <i>D&Optical I/O, IMEC</i>