

2020년 2월 14일(금), 15:45~17:30

Room B (에메랄드 II+III, 5층)

F. Silicon and Group-IV Devices and Integration Technology 분과
[FB3-F] Nano-electromechanical and 3D Integration Technology

FB3-F-1 15:45~16:00	Island-style Monolithic Three-dimensional (M3D) CMOS–NEM Reconfigurable Logic (RL) Circuits Hyug Su Kwon, Ji Wang Ko, and Woo Young Choi <i>Department of Electronic Engineering, Sogang University</i>
FB3-F-2 16:00~16:15	Novel Release Mechanism of Nanoelectromechanical Memory Switches Gwangryeol Baek and Woo Young Choi <i>Department of Electronic Engineering, Sogang University</i>
FB3-F-3 16:15~16:30	Dynamic Slingshot Pull-in Operation of Nanoelectromechanical (NEM) Memory Switches for Low Operating Voltage Min Hee Kang and Woo Young Choi <i>Department of Electronic Engineering, Sogang University</i>
FB3-F-4 16:30~16:45	Low-power Nanoelectromechanical (NEM) Device with HfO₂-based Ferroelectric Capacitor Shinhee Kim ¹ , Jae Yeon Park ¹ , Hyug Su Kwon ² , Woo Young Choi ² , and Sangwan Kim ¹ <i>¹Department of Electrical and Computer Engineering, Ajou University, ²Department of Electrical Engineering, Sogang University</i>
FB3-F-5 16:45~17:00	피드백 전계 효과 트랜지스터를 활용한 적층형 3차원 집적회로 특성 관찰 Jong Hyeok Oh and Yun Seop Yu <i>Department of Electrical, Electronic and Control Engineering, Hankyong National University</i>
FB3-F-6 17:00~17:15	3D NAND 제작을 위한 비인산계 식각액의 선택적 Si₃N₄ 식각 공정 손창진, 임상우 <i>Department of Chemical and Biomolecular Engineering, Yonsei University</i>
FB3-F-7 17:15~17:30	Interface Charge Effects of Monolithic 3D JLFET Inverter Tae Jun Ahn ^{1,2} , Young Baek Kim ² , and YunSeop Yu ¹ <i>¹Department of Electrical, Electronic and Control Engineering, Hankyong National University, ²Group for Nano-photonics Convergence Technology, KITECH</i>