

# [제 26회 한국반도체학술대회] Program at a Glance

2층				
2월 13일(수)	Room A 아라홀	Room B 마루홀	Room C 가람홀	로비
10:30~13:00	[Short Course 1] Emerging Nano-Devices: from Concepts to Technology			등록
13:00~14:00	점심			
14:00~18:00	[Short Course 1] Emerging Nano-Devices: from Concepts to Technology	[Short Course 2] Revolution Engine: Memory Technolgy	[Short Course 3] 자율주행 차량용 LIDAR & Image Sensor	

2층						5층							로비	
2월 14일(목)	그랜드볼룸	Room A 아라홀	Room B 마루홀	Room C 가람홀	Room D 스타홀	Room E 스카이홀	Room F 실버홀	Room G 루비 3	Room H 루비1+2	Room I 매실	Room J 란실	Room K 국실		Room L 죽실
09:00~10:45		TA1-K Neuromorphic Computing	TB1-G Advanced Devices I : Multi-Gate Transistors	TC1-F Advanced Integration Technology and Photonics	TD1-J Neuromorphic System	TE1-J Functional Electronic Materials	TF1-D Thin Film Transistors	TG1-C Oxide (Nitride) Materials	TH1-H Image Engineering & Touch Sensor	TI1-S Selected Papers on Chip Design Contest	TJ1-P Mechanical & Heat Energy Conversion Devices	TK1-N Advances in Design Methodology		등록 & 전시 (골드홀)
10:45~11:00		휴식 (& 커피, 다과)												
11:00~12:30		TA2-K Cross-point Array Technologies	TB2-G Modeling & Simulation I : Reliability	TC2-F Nanowire FETs	TD2-J Hybrid Perovskite Materials	TE2-A Interconnect & Package	TF2-D 2-Dimensional Materials	TG2-C III-V & Materials Growth/Calculations	TH2-H Thin Film Transistor & Encapsulation for Display Applications	TI2-B EUV Lithography I	TJ2-P Phtovoltaic and Device Fabrication Technology		상임운영 위원회 지능형 반도체 기술 개발 세션	
12:30~14:00		점심												
14:00~14:10		개회식												
14:10~15:00		기조강연 1 (3층, 체육관) 박병국 교수 (서울대학교) "신경계와 반도체 집적회로"												
15:00~15:10		휴식												
15:10~16:00		기조강연 2 (3층, 체육관) 이승수 대표이사 (인피니언 테크놀로지스 코리아㈜) "자율주행과 차량용 반도체의 미래 동향"												
16:00~16:10		휴식												
16:10~17:40	포스터세션1													
18:00~20:00		만찬 (3층, 체육관)												
20:00~22:00		[Rump Session 1] (Room A(아라홀, 본관 2층)) 64Kilo에서 100Tera를 향한 미래 반도체의 전망과 도전					[Rump Session 2] (Room B(마루홀, 본관 2층)) 파워반도체 - 미래와 도전							

# [제 26회 한국반도체학술대회] Program at a Glance

2월 15일(금)	2층					5층								로비	
	그랜드볼룸	Room A 아라홀	Room B 마루홀	Room C 가람홀	Room D 스타홀	Room E 스카이홀	Room F 실버홀	Room G 루비 3	Room H 루비1+2	Room I 매실	Room J 란실	Room K 국실	Room L 죽실		
09:00~10:45		FA1-K Resistive Memory I	FB1-G Advanced Devices II : Nano Devices	FC1-F Neuromorphic Devices and Application	FD1-J Organic Semiconductor	FE1-B Advanced Patterning Process	FF1-D Device Fabrication I	FG1-M RF Circuit Design	FH1-E Compound Device Technology	FI1-L Analog Design	FJ1-P Battery and H2 Generation Devices	FK1-R 스토리지 성능 최적화 SW	FL1-SS Special Session: Sensor & IoT	등록 & 전시 (골드홀)	
10:45~11:00	휴식 (& 커피, 다과)														
11:00~12:30		FA2-K Resistive Memory II	FB2-G Advanced Devices III : Technology & Simulation	FC2-F Negative Capacitance and Ferro-Electric FETs	FD2-J Organic Optoelectronic Devices	FE2-B EUV Lithography II	FF2-D Device Fabrication II	FG2-C Materials Characterization & (Zn,Ti)O2	FH2-E Compound Process Technology	FI2-O Artificial Intelligence (AI) Circuits and Systems	FJ2-I Advanced MEMS and Sensor Systems	FK2-Q Metrology, Inspection, and Yield Enhancement	FL2-SS Special Session: 고전력 반도체 & 헬스케어		등록 & 전시 (골드홀)
12:30~14:00	점심														
14:00~15:30	포스터세션2														
15:30~17:15		FA3-K Memory Technologies	FB3-G Modeling & Simulation II : Memory and Other Devices	FC3-F Tunnel FETs and NEM Devices	FD3-J Two Dimensional Semiconducting Materials	FE3-F Advanced Device Technology	FF3-D Thin Film Growth	FG3-C Low-Dimensional Materials (Perovskite, Graphene & Beyond Graphene)	FH3-E Compound Characterization Technology	FI3-O VLSI System Design and Application	FJ3-I MEMS and Sensor Systems for Biomedical Applications	FK3-Q Nanoanalysis and Characterization	FL3-SS Special Session: AI		
17:15~17:30	폐회식														

분과	
A	Interconnect & Package
B	Patterning
C	Material Growth & Characterization
D	Thin Film Process Technology
E	Compound Semiconductors
F	Silicon and Group-IV Devices and Integration Technology
G	Device & Process Modeling, Simulation and Reliability
H	Display and Imaging Technologies
I	MEMS & Sensor Systems
J	Nano-Science & Technology
K	Memory (Design & Process Technology)
L	Analog Design
M	RF and Wireless Design
N	VLSI CAD
O	System LSI Design
P	Device for Energy (Solar Cell, Power Device, Battery, etc.)
Q	Metrology, Inspection, and Yield Enhancement
R	Semiconductor Software
S	Chip Design Contest
SS	Special Session

※ 구두발표 세션기호 보는 법						
요일	목요일	세션룸	세션순서	분과		
T	목요일	A Room A	1 첫번째 세션	A	M	
F	금요일	B Room B	2 두번째 세션	B	N	
		C Room C	3 세번째 세션	C	O	
		D Room D		D	P	
		E Room E		E	Q	
		F Room F		F	R	
		G Room G		G	S	
		H Room H		H	SS	
		I Room I		I		
		J Room J		J		
		K Room K		K		
		L Room L		L		

※ TA1-K: 목요일 Room A에서 첫타임에 진행되는 K분과의 세션  
 ※ FA1-K-1: 금요일 Room A에서 첫타임에 진행되는 K분과 세션의 첫번째 발표

※ 포스터세션 분과 배정			
포스터세션1	TP1	2월 14일(목)	A, C, D, H, K, N, P, S
포스터세션2	FP1	2월 15일(금)	B, E, F, G, I, J, L, M, O, Q, R