'Smart Semiconductor in IoT and 4th Industrial Revolution'

## H D. Thin Film Process Technology 분과

2019년 2월 14일(목), 11:00-12:30 Room F (실버홀, 5층)

## [TF2-D] 2-Dimensional Materials

## 좌장: 안지훈 교수(한국해양대학교), 전우진 교수(경희대학교)

TF2-D-1 11:00-11:15	The Controllable Carrier Polarity of WSe2 Field Effect Transistor Dependent on Contact Metal
	Dain Kang, Taekwang Kim, Somyeong Shin, Hyewon Du, Minho Song, Seonyeong Kim, Hansung Kim, and Sunae Seo <i>Department of Physics, Sejong University</i>
TF2-D-2 11:15-11:45	[초청] New Perspectives in Atomic Layer Deposition Technique for 2D Material Thinfilm Deposition and Thinfilm Deposition on 2D Materials Woojin Jeon Department of Advanced Materials Engineering for Information and Electronics, Kyung Hee University
TF2-D-3 11:45-12:00	Wafer Scale Growth of MoS <sub>2</sub> /GaN Heterostructure Using Metal-Organic Chemical Vapor Deposition for Optoelectronic Application
	Juhun Lee, Hyunwoo Jang, Taemyung Kwak, and Okhyun Nam Department of Nano-Optical Engineering, Korea Polytechnic University
TF2-D-4 12:00-12:15	Wafer-Scale Monolayer $MoS_2$ Growth on $SiO_2$ Substrate Using Modified Atomic Layer Deposition Technique
	Dae Hyun Kim <sup>1</sup> , Dae Woong Kim <sup>2</sup> , Tea Jun Seok <sup>2</sup> , Hyun Soo Jin <sup>2</sup> , Jae Chan Park <sup>2</sup> , and Tae Joo Park <sup>1,2</sup>
	<sup>1</sup> Department of Advanced Materials Engineering, Hanyang University, <sup>2</sup> Department of Materials Science and Chemical Engineering, Hanyang University
TF2-D-5 12:15-12:30	Wafer-Scale Growth of Single Phase $SnS_2$ by Atomic Layer Deposition
	Jung Joon Pyeon <sup>1,2</sup> , In-Hwan Baek <sup>1,3,4</sup> , Taek-Mo Chung <sup>5</sup> , Jeong Hwan Han <sup>6</sup> , Chong-Yun Kang <sup>1,2</sup> , and Seong Keun Kim <sup>1</sup> <sup>1</sup> Center for Electronic Materials, KIST, <sup>2</sup> KU-KIST Graduate School of Converging Science and Technology, Korea University, <sup>3</sup> Department of Materials Science and Engineering, Seoul National University, <sup>4</sup> Inter-University Semiconductor Research Center, Seoul National University, <sup>5</sup> Division of Advanced Materials, KRICT, <sup>6</sup> Department of Materials Science and Engineering, Seoul National University of Science and Technology