



## ★ Special Session

2019년 2월 15일(금), 15:30-17:15

Room L (죽실, 5층)

**[FL3-SS] Special Session: AI**

좌장: 이영주 교수(POSTECH), 김 윤 교수(부산대학교)

<b>FL3-SS-1</b> <b>15:30-15:45</b>	<b>The Effect of Structure and Position of Layer on Accuracy of Hardware Neural Network with Variation of Synapse Devices</b> Hyeongsu Kim <sup>1,2</sup> , Suhwan Lim <sup>1,2</sup> , Jong-Ho Bae <sup>1,2</sup> , Sung-Tae Lee <sup>1,2</sup> , Young-Tak Seo <sup>1,2</sup> , Dongseok Kwon <sup>1,2</sup> , Byung-Gook Park <sup>1,2</sup> , and Jong-Ho Lee <sup>1,2</sup> <sup>1</sup> Department of Electrical and Computer Engineering, Seoul National University, <sup>2</sup> Inter-University Semiconductor Research Center, Seoul National University
<b>FL3-SS-2</b> <b>15:45-16:00</b>	<b>Self-Limited Switching for Analog Data Programming and its Application to a Memristive Neural Network</b> Hanchan Song, Young Seok Kim, Juseong Park, and Kyung Min Kim Department of Materials Science and Engineering, KAIST
<b>FL3-SS-3</b> <b>16:00-16:15</b>	<b>Artificial Optic-Neural Synapse for Colored and Color-Mixed Pattern Recognition</b> Seunghwan Seo and Jin-Hong Park Department of Electrical and Computer Engineering, Sungkyunkwan University
<b>FL3-SS-4</b> <b>16:15-16:30</b>	<b>Performance of Spin Orbit Torque-Driven Electronic Synapse Functions</b> Jeonghun Shin <sup>1</sup> , Seungmo Yang <sup>1</sup> , Jinhyng Choi <sup>1</sup> , Jungyup Yang <sup>2</sup> , and Jinpyo Hong <sup>1</sup> <sup>1</sup> Department of Physics, Hanyang University, <sup>2</sup> Department of Physics, Kunsan National University
<b>FL3-SS-5</b> <b>16:30-16:45</b>	<b>크기에 따른 양자화 방법을 통한 DNN 최적화</b> Joo-Ho Kim and Joon-Sung Yang Department of Electrical and Computer Engineering, Sungkyunkwan University
<b>FL3-SS-6</b> <b>16:45-17:00</b>	<b>A Massive Parallel Multi-Operand Adder for Artificial Neurons</b> Dohyun Kim, Hyunbin Park, and Shiho Kim School of Integrated Technology, Yonsei University
<b>FL3-SS-7</b> <b>17:00-17:15</b>	<b>Approximation Schemes for Energy Efficient Spike-based Computing System</b> Hyeonseong Kim and Jongsun Park Department of Electronic Engineering, Korea University