

 O. System LSI Design 분과

2019년 2월 15일(금), 15:30-17:15

Room I (매실, 5층)

**[FI3-O] VLSI System Design and Application**

좌장: 김경기 교수(대구대학교), 김영민 교수(광운대학교)

<p><b>FI3-O-1</b> 15:30-15:45</p>	<p><b>Design of Multi-Mode Radar Signal Processor for Drone Detection</b> Seung Hyeok Lee, Yongchul Jung, Sora Jin, and Yunho Jung <i>School of Electronics and Information Engineering, Korea Aerospace University</i></p>
<p><b>FI3-O-2</b> 15:45-16:15</p>	<p>[초청] <b>메모리 오류정정 기술</b> 공준진 <i>Samsung Electronics Co., Ltd.</i></p>
<p><b>FI3-O-3</b> 16:15-16:30</p>	<p><b>Low Power Baseband Analog for Sub-GHz Transceiver with Wide Dynamic Range</b> SungJun Jo, Chan ho Kim, Sung-Jin Kim, and Kang-Yoon Lee <i>Department of Electrical and Computer Engineering, Sungkyunkwan University</i></p>
<p><b>FI3-O-4</b> 16:30-16:45</p>	<p><b>Design of CMOS OTDR for Handheld Optic Fiber Scan Systems</b> Keunyeol Park, Hyejin Im, Minkyu Song, and Soo Youn Kim <i>Department of Semiconductor Science, Dongguk University</i></p>
<p><b>FI3-O-5</b> 16:45-17:00</p>	<p><b>고속 무선 비디오 전송을 위한 Interleaved BCH 시스템의 구현</b> 황석하, 문승식, 이영주 <i>포항공과대학교 전자전기공학과</i></p>
<p><b>FI3-O-6</b> 17:00-17:15</p>	<p><b>An Architecture of Power Calculation Hardware for Emulation Based Power Analysis</b> Changwon Choi and Joonhwan Yi <i>Department of Computer Engineering, Kwangwoon University</i></p>