

 O. System LSI Design **분과**

2019년 2월 15일(금), 11:00-12:30

Room I (매실, 5층)

**[FI2-O] Artificial Intelligence (AI) Circuits and Systems**

좌장: 김경기 교수(대구대학교), 김영민 교수(광운대학교)

<p><b>FI2-O-1</b> 11:00-11:15</p>	<p><b>Effect of Pruning on Energy-efficient Spiking Neural Network Trained by Back-Propagation</b> Seongbin Oh<sup>1,2</sup>, Chul-Heung Kim<sup>1,2</sup>, Soo-Chang Lee<sup>1,2</sup>, Jang-Saeng Kim<sup>1,2</sup>, Byung-Gook Park<sup>1,2</sup>, and Jong-Ho Lee<sup>1,2</sup> <i><sup>1</sup>Department of Electrical and Computer Engineering, Seoul National University, <sup>2</sup>Inter-University Semiconductor Research Center, Seoul National University</i></p>
<p><b>FI2-O-2</b> 11:15-11:30</p>	<p><b>Design of RCE Neural Network Accelerator for Hand Gesture Recognition</b> Minwoo Kim, Woojae Jeong, Jaechan Cho, and Yunho Jung <i>School of Electronics and Information Engineering, Korea Aerospace University</i></p>
<p><b>FI2-O-3</b> 11:30-11:45</p>	<p><b>Digital Neuron : A Basic Multiply and Accumulative Element for Artificial Neural Network</b> Hyunbin Park, Dohyun Kim, and Shiho Kim <i>School of Integrated Technology, Yonsei University</i></p>
<p><b>FI2-O-4</b> 11:45-12:00</p>	<p><b>Computation-In-DRAM Circuits for Quantized Neural Networks</b> Khoa Van Pham, Son Bao Tran, Tien Van Nguyen, and Kyeong-Sik Min <i>School of Electrical Engineering, Kookmin University</i></p>
<p><b>FI2-O-5</b> 12:00-12:15</p>	<p><b>Processing In-Memory of BCH Encoder Using a 9T SRAM Array with XOR/Write-back/SHIFT Operation</b> Geon Ko, Taehwan Kim, Kyeongho Lee, Yunho Jang, and Jongsun Park <i>School of Electrical Engineering, Korea University</i></p>
<p><b>FI2-O-6</b> 12:15-12:30</p>	<p><b>FPGA Implementation of the Simplified Digital Spiking Neural Network</b> Kyungpil Lee<sup>1</sup> and Youngmin Kim<sup>2</sup> <i><sup>1</sup>Department of Electronic Engineering, Kwangwoon University, <sup>2</sup>School of Computer and Information Engineering, Kwangwoon University</i></p>