

 L. Analog Design 분과

2019년 2월 15일(금), 09:00-10:15

Room I (매실, 5층)

[F11-L] Analog Design

좌장: 김종선 교수(홍익대학교), 지동우 교수(아주대학교)

<p><b>F11-L-1</b> 09:00-09:15</p>	<p><b>A CMOS Optical Sensor for High-Sensitivity Current Sensing Applications</b> Ying He, Chaerin Hong, Daseul Yoon, Yoonji Park, Sung Min Park, and Jihoon Kim <i>Department of Electronic and Electrical Engineering, Ewha Womans University</i></p>
<p><b>F11-L-2</b> 09:15-09:30</p>	<p><b>Low-Noise CMOS Transimpedance Amplifier with Nested Chopper</b> Younggyun Oh, Sein Oh, Dongyoung Hwang, and Hyungil Chae <i>Department of Electronic Engineering, Kookmin University</i></p>
<p><b>F11-L-3</b> 09:30-09:45</p>	<p><b>PVT-Tolerant Dynamic Amplifier</b> Sein Oh and Hyungil Chae <i>School of Electronic Engineering, Kookmin University</i></p>
<p><b>F11-L-4</b> 09:45-10:00</p>	<p><b>A Simple Scan Driver Circuit for Compensating a Threshold Voltage of the Pull-Down Transistors</b> Jongsu Oh<sup>1</sup>, Kee Chan Park<sup>2</sup>, Jae-Hong Jeon<sup>3</sup>, and Yong-Sang Kim<sup>1</sup> <sup>1</sup><i>Department of Electrical and Computer Engineering, Sungkyunkwan University,</i> <sup>2</sup><i>Department of Electronics Engineering, Konkuk University,</i> <sup>3</sup><i>School of Electronics and Information Engineering, Korea Aerospace University</i></p>
<p><b>F11-L-5</b> 10:00-10:15</p>	<p><b>A 75.3dB DR, 6.25MHz BW Discrete-Time 2<sup>nd</sup> Order Delta-Sigma Modulator</b> Jun-Ho Boo, Yong-Sik Kwak, Kang-Il Cho, Ho-Jin Kim, and Gil Cho Ahn <i>Department of Electronic Engineering, Sogang University</i></p>