

# Program at a Glance

제 25회 한국반도체학술대회 (KCS 2018)  
2018년 2월 5일(월)-7일(수) / 강원도 하이원리조트 컨벤션 호텔

2월 5일(월)	5층			5층			5층			5층		
	컨벤션홀 L			태백룸			함백룸					
14:00-18:00	[Short Course 1] 뉴로모픽(Neuromorphic) 기술의 이해			[Short Course 2] Quantum Computing			[Short Course 3] SI-PI-EMI Analysis of Advanced Semiconductor Packaging Technologies					
2월 6일(화)	5층				6층						5층	
	Room A 태백I	Room B 태백II+III	Room C 함백	Room D 함백II+III	Room F 봉래	Room G 봉래II+III	Room H 청옥	Room I 청옥II+III	Room J 육백I	Room K 육백II		로비
08:15-09:00				TD0-S Chip Design Contest								
09:00-10:45	TA1-A Emerging Interconnect	TB1-I Gas/Chemical Sensors	TC1-D Oxide Thin Film Transistor	TD1-R 고성능 스토리지 기술	TF1-F Neuromorphic Device and Application	TG1-G Advanced Devices I - Technology and Simulation	TH1-J Graphene Related Nano Materials	TI1-K ReRAM I - Preparing for Mass Production	TJ1-M RF and Wireless System and Circuits I	TK1-Q Metrology & Inspection	전시	
10:45-10:55	휴식 (& 커피, 다과)											
10:55-11:00	개회식 [컨벤션홀 K+W / 5층]											
11:00-12:00	기조강연 1: Prof. Sanjay Banerjee (University of Texas at Austin) "Electronics in Flatland" [컨벤션홀 K+W / 5층]											
12:00-13:00	점심 [포레스트볼룸 / 4층]											
13:00-14:00	기조강연 2: 김진형 원장 (AIR(인공지능연구원)) "인공지능의 능력과 한계" [컨벤션홀 K+W / 5층]											
14:00-14:10	휴식 (& 커피, 다과)											
14:10-15:55	TA2-A FOWLP & Reliability	TB2-I Advanced Sensor Systems	TC2-D Emerging Thin Film Technology	TD2-R 시스템 소프트웨어 응용	TF2-F Intergration Technology	TG2-G Modeling and Simulation I - Nano Devices	TH2-J Two Dimensional Nano Materials	TI2-K Devices for Neuromorphic Computing	TJ2-M RF and Wireless System and Circuits II	TK2-Q Nanoanalysis		
16:00-17:30											포스터세션1 [TP1]	
17:40-20:00	만찬 [컨벤션홀 K+W / 5층]											
20:00-	[Rump Session 1] 한국 시스템 반도체 산업의 미래			[Rump Session 2] 한국 메모리 반도체의 미래/차세대 메모리 반도체 기술								
2월 7일(수)	5층				6층						5층	
	Room A 태백	Room B 태백II+III	Room C 함백	Room D 함백II+III	Room F 봉래	Room G 봉래II+III	Room H 청옥	Room I 청옥II+III	Room J 육백	Room K 육백II		로비
09:00-10:30	WA1-C Material Growth and Characterization I	WB1-SS Special Session: 인공지능	WC1-D ALD/CVD Process (2D Materials)	WD1-R 다양한 소프트웨어 최적화 기술	WF1-F Steep-Slope I : Tunnel-FET	WG1-G Advanced Devices II - Simulation and Reliability	WH1-B 리소그래피 및 플라즈마에 청	WI1-K Topics Related to Memory Design	WJ1-LM Analog & RF Circuits	WK1-Q Inspection & Yield Enhancement	전시	
10:30-10:45	휴식 (& 커피, 다과)											
10:45-12:15	WA2-C Material Growth and Characterization II	WB2-SS Special Session: IoT I	WC2-D Thin Films for Memories and Transistors I	WD2-E III-V Emerging Device	WF2-F Reliability	WG2-G Advanced Devices III - Simulation and Reliability	WH2-J Nano Materials and Nano Structures	WI2-K ReRAM II - New Technologies	WJ2-L Analog Circuit Design	WK2-O VLSI System Design and Application I		
12:15-13:15	점심 [포레스트볼룸 / 4층]											
13:15-14:45	WA3-P Device for Solar Energy Conversion	WB3-SS Special Session: IoT II	WC3-I Flexible Sensor Systems	WD3-E GaN Device	WF3-F Photonics and Nanowire Technology	WG3-G Modeling and Simulation II - Device and Process	WH3-J CNT Related Nanotechnology	WI3-K FeRAM and Transparent ReRAM	WJ3-N IoT & SoC Methodology	WK3-O VLSI System Design and Application II		
14:45-16:15	휴식 (& 커피, 다과)					휴식 (& 커피, 다과)					포스터세션2 [WP1]	
16:15-17:30	WA4-P Fabrication for Functional Energy Device	WB4-H Display and Imaging	WC4-D Thin Films for Memories and Transistors II	WD4-E SiC Device	WF4-F Steep-Slope II : NC-FET	WG4-G Memory and TFT - Modeling and Characterization	WH4-J General Nano Technology	WI4-K Phase-Change Memory	WJ4-N Test & Reliability		전시	
17:30-17:40	폐회식 및 경품추첨 [6층, 육백룸]											