



제25회 한국반도체학술대회

The 25th Korean Conference on Semiconductors

2018년 2월 5일(월)-7일(수), 강원도 하이원리조트 컨벤션 호텔

2018년 2월 7일(수), 13:15-14:45

Room K (육백II, 6층)

O. System LSI Design 분과

[WK3-O] VLSI System Design and Application II

좌장: 김경기 교수(대구대학교), 김수연 교수(동국대학교)

WK3-O-1 13:15-13:30	Design of FMCW Radar Signal Processor for Drone Altitude Measurement Yongchul Jung, Euibeen Lim, Sora Jin, and Yunho Jung <i>School of Electronics and Information Engineering, Korea Aerospace University</i>
WK3-O-2 13:30-13:45	Design of a High Precision Ramp Generator for a 14-bit Single-Slope ADC Hyeonseob Noh, Sooyoun Kim, and Minkyu Song <i>Department of Semiconductor Science, Dongguk University</i>
WK3-O-3 13:45-14:00	Efficient Reconfigurable Architecture to Accelerate Descriptor Extraction in SURF Algorithm Yoonjin Kim and Haelim Jung <i>Department of Computer Science, Sookmyung Women's University</i>
WK3-O-4 14:00-14:15	A 25-Gbps Low-Power PAM-4 Transmitter in 28-nm CMOS Minkyu Kim, Dae-Hyun Kwon, Sung-Geun Kim, and Woo-Young Choi <i>Department of Electrical and Electronic Engineering, Yonsei University</i>
WK3-O-5 14:15-14:30	Radiation Hardened Microprocessor Design Using Spatial and Temporal Dual Modular Redundancy Jun Sung Go, Jong Kang Park, Jong Tae Kim <i>School of Electronic and Electrical Engineering, Sungkyunkwan University</i>
WK3-O-6 14:30-14:45	Reducing FPGA Area Using Nano-Switch Devices in Inter and Intra-Logic Routing. Aidyn Zhakatayev and Jongeun Lee <i>School of Electrical & Computer Engineering, UNIST</i>