



제25회 한국반도체학술대회

The 25th Korean Conference on Semiconductors

2018년 2월 5일(월)-7일(수), 강원도 하이원리조트 컨벤션 호텔

2018년 2월 7일(수), 13:15-14:45

Room J (육백, 6층)

N. VLSI CAD 분과

[WJ3-N] IoT & SoC Methodology

좌장: 양준성 교수(성균관대학교), 정재용 교수(인천대학교)

WJ3-N-1 13:15-13:45	[초청] Toward IoT Era Enabled by AI Lokwon Kim <i>Department of Computer Science, KyungHee University</i>
WJ3-N-2 13:45-14:00	IBS 방식을 이용한 PUF 기반의 암호화 키 생성 메커니즘 단순화 조호준, 양준성 <i>성균관대학교 반도체디스플레이공학과</i>
WJ3-N-3 14:00-14:15	Low Overhead TSV Repair for TSV-Induced Noise and Stress Reduction Muhammad Imran and Joon-Sung Yang <i>Department of Electrical and Computer Engineering, Sungkyunkwan University</i>
WJ3-N-4 14:15-14:30	Automatic Clock Gating Synthesis through Detection of Cyclic Paths Yuepeng Fan, Inhak Han, and Youngsoo Shin <i>School of Electrical Engineering, KAIST</i>
WJ3-N-5 14:30-14:45	Fast Timing Analysis of Full Custom Digital Circuits with Accurate Gate RC Modeling Jingon Lee, Jinwook Jung, and Youngsoo Shin <i>School of Electrical Engineering, KAIST</i>