2018년 2월 7일(수), 09:00-10:30 Room F (봉래I, 6층)

F. Silicon and Group-IV Devices and Integration Technology 분과 [WF1-F] Steep-Slope I: Tunnel-FET

좌장: 김경록 교수(UNIST), 김상완 교수(아주대학교)

WF1-F-1 09:00-09:15	Tunneling Field Effect Transistors with FIN-typed Channel Structure and Their Electrical Characteristics Donghwan Lim, Hoon Hee Han, and Changhwan Choi Division of Materials Science and Engineering, Hanyang University
WF1-F-2 09:15-09:30	Double-Gate Isosceles Trapezoid Tunnel Field-Effect Transistor (DGIT-TFET) to Suppress Ambipolar Current Hwa Young Gu and Sangwan Kim Department of Electrical and Computer Engineering, Ajou University
WF1-F-3 09:30-09:45	Segmented-Channel Tunnel Field Effect Transistor for Bi-Directional Current Flow Jaesoo Park, Sungjin Lee, and Changhwan Shin Department of Electrical and Computer Engineering, University of Seoul
WF1-F-4 09:45-10:00	Drive Current Boosting Method of Tunnel FET with Locally Concentrated Silicon-Germanium Channel near Surface Junil Lee ¹ , Ryoongbin Lee ¹ , Euyhwan park ¹ , Sihyun Kim ¹ , Hyun-Min Kim ¹ , Kitae Lee ¹ , Soyoun Kim ¹ , Sangwan Kim ² , and Byung-Gook Park ¹ ¹ Department of Electrical and Computer Engineering and ISRC, Seoul National University, ² Department of Electrical and Computer Engineering, Ajou University
WF1-F-5 10:00-10:15	Tunneling Field-Effect Transistor Having SiGe Source Junction and Its Small-Signal Equivalent Circuit Verification through Y-Parameter Analysis Yung Hun Jung ¹ , In Man Kang ² , Wookyung Sun ³ , Hyungsoon Shin ³ , and Seongjae Cho ¹ ¹ Department of Electronics Engineering, Gachon University, ² School of Electronics Engineering, Kyungpook National University, ³ Department of Electronic and Electrical Engineering, Ewha Woman's University
WF1-F-6 10:15-10:30	Nanowire Tunnel Field-Effect Transistor (TFET) with Ultra-Thin-Tunnel Region for High Current Drivability and Low Subthreshold Swing Seong-Hyun Lee, Jeong-Uk Park, and Sangwan Kim Department of Electrical and Computer Engineering, Ajou University