



# 제25회 한국반도체학술대회

The 25<sup>th</sup> Korean Conference on Semiconductors

2018년 2월 5일(월)-7일(수), 강원도 하이원리조트 컨벤션 호텔

2018년 2월 6일(화), 08:15-09:00

Room D (함백II+III, 5층)

## S. Chip Design Contest 분과

### [TD0-S] Chip Design Contest

좌장: 김태욱 교수(연세대학교), 차혁규 교수(서울과학기술대학교)

TD0-S-1 08:15-08:30	<b>65nm RF CMOS Gilbert-Cell Mixer for Wireless Power Transfer System</b> Ju-Hwan Lim, Nhut-Tan Doan, Wan-Su Kim, Ngoc-Duy-Hien Lai, and Sang-Woong Yoon <i>Department of Electronics Engineering, Kyung Hee University</i>
TD0-S-2 08:30-08:45	<b>A Low Jitter Clock and Data Recovery Circuit with Pulse-Width-Adjusting Binary Phase Detector Using 180-nm CMOS</b> Jaeok Yun, Sanghun Baek, Jaepil Park, and Jin-Ku Kang <i>Department of Electronics Engineering, Inha University</i>
TD0-S-3 08:45-09:00	<b>A 1 MHz 1 M<math>\Omega</math> Output Impedance CMOS Current Driver and Active Electrode for ECG/ETI Measurement</b> Xuan Tien Nguyen, Woo-Jin Cho, Jun-Hyeong Kwon, Pham Thanh Son, and Jong-Wook Lee <i>Department of Electronics Engineering, Kyung Hee University</i>