



제25회 한국반도체학술대회

The 25th Korean Conference on Semiconductors

2018년 2월 5일(월)-7일(수), 강원도 하이원리조트 컨벤션 호텔

2018년 2월 7일(수), 10:45-12:15

Room K (육백II, 6층)

O. System LSI Design 분과

[WK1-Q] VLSI System Design and Application I

WK2-O-1 10:45-11:00	A Proposal of Learning Method Using Spike-Timing Dependent Plasticity for Neuromorphic Systems Sungmin Hwang, Hyungjin Kim, Min-Woo Kwon, Jungjin Park, and Byung-Gook Park <i>Department of Electrical Engineering, Seoul National University</i>
WK2-O-2 11:00-11:15	Segmentation Based Disparity Refinement for Stereo Matching Gyujin Bae and Young Hwan Kim <i>Department of Electrical Engineering, POSTECH</i>
WK2-O-3 11:15-11:30	Unsupervised Learning of Image Patterns Using Multiple Postsynaptic Neurons based on Spike-Timing-Dependent Plasticity Soochang Lee, Chul-Heung Kim, Byung-Gook Park, and Jong-Ho Lee <i>Department of Electrical and Computer Engineering, Inter-University Semiconductor Research Center, Seoul National University</i>
WK2-O-4 11:30-11:45	Accelerator Design of Concurrent Internet Key Exchange Protocol Aimed at Heavy Traffic with Multi Users Saad Arslan, Shima Abdel Naby Abdel Hakim, and HyungWon Kim <i>Department of Electronic Engineering, Chungbuk National University</i>
WK2-O-5 11:45-12:00	Bitwise Convolution Circuit for Processing-In-DRAM Khoa Van Pham, Son Bao Tran, Tien Van Nguyen, HyunKyung Nam, and Kyeong-Sik Min <i>School of Electrical Engineering, Kookmin University</i>
WK2-O-6 12:00-12:15	High Voltage Neurostimulator in Low Voltage CMOS Process Jehoon Kim, Doojin Jang, Miryeong Seol, and Minkyu Je <i>School of Electrical Engineering, KAIST</i>