



제25회 한국반도체학술대회

The 25th Korean Conference on Semiconductors

2018년 2월 5일(월)-7일(수), 강원도 하이원리조트 컨벤션 호텔

2018년 2월 7일(수), 10:45-12:15

Room I (청옥II+III, 6층)

K. Memory (Design & Process Technology) 분과 [WI2-K] ReRAM II - New Technologies

WI2-K-1 10:45-11:15	[초청] Halide perovskites for Low Voltage Resistive Switching Memories Ho Won Jang <i>Department of Materials Science and Engineering, Seoul National University</i>
WI2-K-2 11:15-11:30	Thermally Stable Resistive Switching Characteristics of Te-Based Conductive-Bridge Memory by Optimizing Zr-Te Composition Sangmin Lee, Seokjae Lim, Jeonghwan Song, Jaehyuk Park, and Hyunsang Hwang <i>Department of MS&E, POSTECH</i>
WI2-K-3 11:30-11:45	Fabrication of Cu Cone Structure Embedded CBRAM Array for Inducing Field Concentration Effect & Material Limited Switching Effect Hae Jin Kim ¹ , Tae Hyung Park ¹ , Young Jae Kwon ¹ , Dae Eun Kwon ¹ , Yu Min Kim ¹ , Tae Jung Ha ² , Soo Gil Kim ² , and Cheol Seong Hwang ¹ <i>¹Department of Materials Science and Engineering and Inter-University Semiconductor Research Center, Seoul National University, ²SK Hynix Inc.</i>
WI2-K-4 11:45-12:00	The Effect of Au Nanodots Geometry and Location in the Pt/Ta₂O₅/HfO₂-x/TiN Structure Young Jae Kwon, Jung Ho Yoon, Yu Min Kim, Dae Eun Kwon, Tae Hyung Park, Hae Jin Kim, Kyung Seok Woo, Tae Gyun Park, and Cheol Seong Hwang <i>Department of Materials Science and Engineering and Inter-university Semiconductor Research Center, Seoul National University</i>
WI2-K-5 12:00-12:15	Self-Rectifying Resistive Random-Access Memory with a-Si/Si₃N₄ Bilayer Hui Tae Kwon, Won Joo Lee, Hyun-seok Choi, Daehoon Wee, Yu Jeong Park, Boram Kim, and Yoon Kim <i>Department of Nano-Energy Engineering, BK21 Plus Nano-convergence Technology Division, Pusan National University</i>