



# 제25회 한국반도체학술대회

The 25<sup>th</sup> Korean Conference on Semiconductors

2018년 2월 5일(월)-7일(수), 강원도 하이원리조트 컨벤션 호텔

2018년 2월 7일(수), 16:15-17:30

Room H (청옥1, 6층)

## J. Nano-Science & Technology 분과

### [WH4-J] General Nano Technology

<p>WH4-J-1 16:15-16:30</p>	<p><b>Fabrication of Ag/ZnO Core/Shell Nanoparticles by Rotational Atomic Layer Deposition and Their Enhanced Photocatalytic Properties</b> Sejong Seong<sup>1</sup>, Yong Chan Jung<sup>1</sup>, Taehoon Lee<sup>1</sup>, Seonyong Kim<sup>1</sup>, In-Sung Park<sup>1,2</sup>, and Jinho Ahn<sup>1,2</sup> <i><sup>1</sup>Division of Materials Science and Engineering, Hanyang University, <sup>2</sup>Institute of Nano Science and Technology, Hanyang University</i></p>
<p>WH4-J-2 16:30-16:45</p>	<p><b>Flexible Phase Change Memory Patterned by Block Copolymer Self-Assembly</b> Gyeong Cheol Park, Beom Ho Mun, and Keon Jae Lee <i>Department of Materials Science and Engineering, KAIST</i></p>
<p>WH4-J-2 16:30-16:45</p>	<p><b>Flexible Phase Change Memory Patterned by Block Copolymer Self-Assembly</b> Gyeong Cheol Park, Beom Ho Mun, and Keon Jae Lee <i>Department of Materials Science and Engineering, KAIST</i></p>
<p>WH4-J-3 16:45-17:00</p>	<p><b>Influence of Self-Heating Effect on Interface Trap Generation in Highly Flexible Single-Crystalline Si Nanomembrane Transistors</b> Jae Hoon Bong<sup>1</sup>, Seung-Yoon Kim<sup>1</sup>, Chan Bae Jeong<sup>2</sup>, Ki Soo Chang<sup>2</sup>, Wan Sik Hwang<sup>3</sup>, and Byung Jin Cho<sup>1</sup> <i><sup>1</sup>School of Electrical Engineering, KAIST, <sup>2</sup>Division of Scientific Instrumentation, Korea Basic Science Institute, <sup>3</sup>Department of Materials Engineering, Korea Aerospace University</i></p>
<p>WH4-J-4 17:00-17:15</p>	<p><b>Mechanical and Electrical Reliability of NMP Optimized Flexible Si CMOS IC</b> Seung-Yoon Kim<sup>1</sup>, Cheolgyu Kim<sup>2</sup>, Jae Hoon Bong<sup>1</sup>, Wan Sik Hwang<sup>3</sup>, Taek-Soo Kim<sup>2</sup>, Jae Sub Oh<sup>4</sup>, and Byung Jin Cho<sup>1</sup> <i><sup>1</sup>School of Electrical Engineering, KAIST, <sup>2</sup>Department of Mechanical Engineering, KAIST, <sup>3</sup>Department of Materials Engineering, Korea Aerospace University, <sup>4</sup>Department of Nano-process, NNFC</i></p>
<p>WH4-J-5 17:15-17:30</p>	<p><b>Y<sub>3</sub>Al<sub>5</sub>O<sub>12</sub>:Ce<sup>3+</sup> (YAG:Ce<sup>3+</sup>)형광판 위 은 나노 입자를 포함한 이차원 광 결정 형성하여 백색 발광 다이오드 발광 효율 개선</b> 김효준<sup>1</sup>, 박인성<sup>2</sup>, 고기영<sup>3</sup>, 안진호<sup>1,2</sup> <i><sup>1</sup>한양대학교, 신소재공학과, <sup>2</sup>한양대학교 나노반도체공학과, <sup>3</sup>한국특허정보원</i></p>