2018년 2월 7일(수), 09:00-10:30
Room B (태백II+III, 5층)

Special Session
[WB1-SS] Special Session: 인공지능

WB1-SS-1 09:00-09:25	**Solving Overlapping Pattern Issues by Inhibitory Synaptic Transistors in Bio-Inspired Neuromorhpic System** Hyungjin Kim, Sungmin Hwang, Seunghyun Kim, Jong-Ho Lee, and Byung-Gook Park *ISRC and Department of Electrical and Computer Engineering, Seoul National University*
WB1-SS-2 09:15-09:30	**Domain Wall Motion-Based Synaptic Behavior Controlled by Spin-Orbit Torque in Magnetic Tunnel Junctions.** SeungMo Yang[1], Jinhyung Choi[1], Wonsup Shin[1], and JinPyo Hong[1,2] *[1]Novel Functional Materials and Devices Lab, The Research Institute for Natural Science, Department of Physics, Hanyang University, [2]Division of Nano-Scale Semiconductor Engineering, Hanyang University*
WB1-SS-3 09:30-09:45	**Self-Rectifying Artificial Synaptic Behavior Observed in Tantalum Oxide Based Memristor** Gwang Ho Baek[1], Tae Yoon Kim[2], Gabriel Jang[2], Da Seul Hyeun[2], and Jin Pyo Hong[1,2] *[1]Division of Nanoscale Semiconductor engineering, Hanyang University, [2]The research Institute for Natural Science, Novel Functional Materials and Devices Lab, Department of Physics, Hanyang University*
WB1-SS-4 09:45-10:00	**Inference Accuracy of Hardware-Based Neural Networks Considering Synaptic Device Variation** Dongseok Kwon, Jongho Bae, Suhwan Lim, Jai-ho Um, Seongtae Lee, and Jong-Ho Lee *Department of Electrical and Computer Engineering and Inter-University Semiconductor Research Center, Seoul National University*
WB1-SS-5 10:00-10:15	**Energy Efficient Spike-Timing Dependent Plasticity Rule for Unsupervised Learning** Donghyeon Cho, Gyuseong Kang, Heetak Kim, Yunho Jang, and Jongsun Park *School of Electrical Engineering, Korea University*
WB1-SS-6 10:15-10:30	**An Energy-Efficient and Low Area CNN Accelerator based on Combined Weight Type Quantization** Nahsung Kim, Dongyeob Shin, Wonseok Choi, Bohun Kim, and Jongsun Park *School of Electronic Engineering, Korea University*