



# 제25회 한국반도체학술대회

The 25<sup>th</sup> Korean Conference on Semiconductors

2018년 2월 5일(월)-7일(수), 강원도 하이원리조트 컨벤션 호텔

2018년 2월 6일(화), 14:10-15:55

Room K (육백II, 6층)

## Q. Metrology, Inspection, and Yield Enhancement 분과

### [TK2-Q] Nanoanalysis

|                        |   |
|------------------------|---|
| TK2-Q-1<br>14:10-14:25 | <b>Dynamic Thin Film Thickness Measurement based on Snapshot Spectro-Ellipsometry</b><br>Vamara Dembele <sup>1</sup> , Inho Choi <sup>1</sup> , Madhan Jayakumar Paul <sup>1</sup> , Sukhyun Choi <sup>1</sup> , Junho Kim <sup>1</sup> , Won Chegal <sup>2</sup> , and Daesuk Kim <sup>1</sup><br><i><sup>1</sup>Division of Mechanical System Engineering, Chonbuk National University, <sup>2</sup>Advanced Instrumentation Institute, Korea Research Institute of Standards &amp; Science</i> |
| TK2-Q-2<br>14:25-14:55 | <b>[초청]</b><br><b>미정</b><br>고대홍<br>연세대학교  |
| TK2-Q-3<br>14:55-15:25 | <b>[초청]</b><br><b>Applications of TEM Electron Energy-Loss Spectroscopy (EELS) Analysis for Materials in Semiconductor Devices</b><br>Jucheol Park, Jeong Eun Chae, Ji-Soo Kim, SangYeol Nam, and Min-Soo Kim<br><i>Materials Characterization Center, Gumi Electronics &amp; Information Technology (GERI)</i>   |
| TK2-Q-4<br>15:25-15:40 | <b>Wafer 표면 Roughness에 따른 Thermal Oxide 영향 연구</b><br>정성우, 박정길, 김자영, 강희복<br><i>SK Siltron</i>  |
| TK2-Q-5<br>15:40-15:55 | <b>Detection of Metal Contamination in the Layer of Silicon Wafers</b><br>Seung-Ik Jo, Ji-Yeon Lim, Sung-wook Lee<br><i>SK Siltron</i>  |