2018년 2월 7일(수), 14:45-16:15 컨벤션 호텔, 5층 로비

[WP1] Poster Session II

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WP1-2	Novel Method for Etch Loading Control of Source and Drain Recess for Formation of Steep Junction Seung-Soo Hong, K. S. Min, B. R. Lim, A. R. Ji, H. H. Jung, G. J. Seong, J. Y. Lee, Y. M. Oh, and J.C. Park Process Development Team, Semiconductor R&D Center
WP1-3	Quasi Atomic Layer Etching of SiO ₂ Layers for Surface Cleaning of Nanoscale Patterns Yongjae Kim ¹ , Kyongbeom Koh ² , Taehwan Cha ² , and Heeyeop Chae ^{1,2} 1 SAINT, Sungkyunkwan University, 2 School of Chemical Engineering, Sungkyunkwan University
WP1-4	Laser Writing for Self-Assembly of Block Copolymer on Graphene Jin Young Choi, Hyeong Min Jin, Seung Hyun Lee, Ju Young Kim, Seung Keun Cha, and Sang Ouk Kim Department of Materials Science and Engineering, KAIST
WP1-5	Complex 3D Multimetal Nanomesh Patterns via Block Copolymer Self-Assembly Seung Keun Cha and Sang Ouk Kim Department of Materials Science & Engineering, KAIST
WP1-6	Enhancement of Gas Sensing Performance through the Decoration of Branch Heterointerfaces and Metallic Effects Hyoun Woo Kim, Myung Sik Choi, and Jae Hoon Bang Division of Materials Science and Engineering, Hanyang University
WP1-7	3D NAND Flash Transistor CD Uniformity 개선 방법 연구 Kibok Kim, Hyoungsoon Yune, Seyoung Oh, Chanha Park, and Hyunjo Yang R&D Division, SK Hynix
WP1-8	CD Loading Mechanism of Si Removal Etch Back at High Selectivity Hyunji Kim ¹ , Yongho Jeon ¹ , Hunsang Kim ² , Fenglin Wang ² , Yongseok Lee ¹ , Dongseok Lee ¹ , Junghyeon Kim ¹ , Lina Yoo ¹ , Seongwoo Myeong ¹ , Miri Jung ¹ , Youngmook Oh ¹ , and Jongcheol Park ¹ 1 Samsung Electronics Co., Ltd., 2 Applied Materials Inc.

WP1-9	Etch Characteristics of Micrometer-Scale Masked Cu Thin Films Using Inductively Coupled Plasma of H ₂ /Ar Jae Sang Choi, Doo Hyeon Cho, Eun Taek Lim, and Chee Won Chung Department of Chemical Engineering, Inha University
WP1-10	Etch Characteristics of Nanometer-Scale Patterned Thin Film Using Aerosol Jet Etching System Doo Hyeon Cho, Eun Taek Lim, Jae Sang Choi, and Chee Won Chung Department of Chemical Engineering, Inha University
WP1-11	Inductively Coupled Plasma Reactive Ion Etching of Cu Thin Film Using O ₂ /Ar Plasma Eun Taek Lim, Jae Sang Choi, Doo Hyeon Cho, and Chee Won Chung Department of Chemical Engineering, Inha University
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WP1-13	Implant Condition에 따른 Gate Poly Etch 기술 Dae Won Kim, Sung Hwan Ahn, Dong Ryeol Lee, and Dong Goo Choi DRAM Process Group, SK Hynix
WP1-14	Characterization of Cr etch process using O ₂ -less Cl ₂ plasma JiCheol Kim, HyunDuck Shin, HoYong Jung, SangPyo Kim, and HyunJo Yang SK Hynix Inc.
WP1-15	Development of Lift-off Patterning Technology of Graphene-ZnO Thin Film on Plastic Substrates for Transparent and Flexible Thin Film Transistors Soon Yeol Kwon ¹ , Dong Geon Jung ² , Young Chan Choi ³ , Jae Yong Lee ⁴ , and Senog Ho Kong School of Electronics Engineering, Kyngpook National University
WP1-16	The Improvement of Plasma Damage Effect by Optimizing PAD Etch Process Jeonggyu Park, Kwangil Choi, Jina Eum, Hongik Kim, Sunggon Choi, and Inwook Cho PMIC Product Engineering, SKHynix systemic Inc
WP1-17	Pulse방식을 이용하여 UHV CVD로 증착한 Epitaxial Si:B박막의 Low Doping 연구이다윤, 박지우, 김태호, 손현철 연세대학교 공과대학 신소재공학과

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WP1-24	Improved Performance of Ge Single Junction Solar Cells with Nano-Pillar Arrays Kangho Kim ¹ , Youngjo Kim ^{1,2} , Nguyen Dinh Lam ¹ , Sunghyun Moon ¹ , and Jaejin Lee ¹ ¹ Department of Electrical and Computer Engineering, Ajou University, ² Korea Advanced Nano Fab Center, ³ Department of Physics, Hanoi National University of Education
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WP1-33	Enhanced Photo-Responses in Multi-Layer MoS ₂ Phototransistors Using Au Nano-Pattern Jinwu Park, Sanghyun Lee, Yeojun Yun, Jaejin Lee, and Junseok Heo Department of Electrical and Computer Engineering, Ajou University
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WP1-39	Transfer Length of an AlGaN/GaN Schottky Contact Yong Woon Park ¹ , Jeong Jin Kim ² , and Jeon Wook Yang ¹ ¹ Department of Semiconductor Science and Technology/Semiconductor Physics Research Center, Chonbuk National University, ² Compound Semiconductor Division, ETRI
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	¹ KITECH, ² School of Electronics Engineering, Kyungpook National University, ³ Department
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