O. System LSI Design 분과

2017년 2월 15일 (수), 10:10-11:40 Room H (다이아몬드1, 3층)

[WH2-O] VLSI System Design and Applications 2

좌장: 이영주(광운대학교), 박종선(고려대학교)

WH2-O-1 10:10-10:25	Area Efficient Neuromorphic Circuit based on Stochastic Computation Kiwon Yoon, Suhyeong Choi, and Youngsoo Shin School of Electrical Engineering, KAIST
WH2-O-2 10:25-10:40	Demonstration of Unsupervised Learning with Spike-Timing Dependent Plasticity for Neuromorphic System
	Chul-Heung Kim, Suhwan Lim, Sung Yun Woo, Byung-Gook Park, and Jong-Ho Lee Department of Electrical and Computer Engineering, Seoul National University, Inter-University Semiconductor Research Center, Seoul National University
WH2-O-3 10:40-10:55	Approximate Computation Sharing Multiplier for Low Power Convolutional Neural Networks Design
	Cheolhwan Kim, Gyuseong Kang, and Jongsun Park School of Electrical Engineering, Korea University
WH2-O-4 10:55-11:10	인공 신경망을 이용한 차선 곡률 검출에 대한 연구
	이보경, 김기주, 이영주 <i>광운대학교 전자공학과</i>
WH2-O-5 11:10-11:25	Moving Object Detector for UAV Collision Avoidance
	Jaechan Cho and Yunho Jung School of Electronics and Information Engineering, Korea Aerospace University
WH2-O-6 11:25-11:40	Low Power Asynchronous NCL Pipeline Design
	Jin Kyung Lee and Kyung Ki Kim Department of Electronic Engineering, Daegu University