

O. System LSI Design 분과

2017년 2월 15일 (수), 08:30-10:00
Room H (다이아몬드1, 3층)

[WH1-O] VLSI System Design and Applications 1

좌장: 김경기(대구대학교), 김지훈(서울과학기술대학교)

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| WH1-O-1 08:30-08:45 | Area-Efficient SC FFT with CORDIC-based Approximations Seokha Hwang and Youngjoo Lee <i>Department of Electronics Engineering, Kwangwoon University</i> |
| WH1-O-2 08:45-09:00 | Low-Complexity 3-Channel FFT Processor for Hand Gesture Recognition Radar Systems Yongchul Jung, Euibeen Lim, and Yunho Jung <i>School of Electronics and Information Engineering, Korea Aerospace University</i> |
| WH1-O-3 09:00-09:15 | ASIC based DSP Platform for a High Performance Hearing-Aid SoC Minjoon Kim, Jaehyuk So, Sangwon Kim, Byungcheol Kang, and Jaeseok Kim <i>Department of Electrical and Electronic Engineering, Yonsei University</i> |
| WH1-O-4 09:15-09:30 | 주성분 분석을 위한 저전력 행렬 곱셈 가속기 설계 변우석 ¹ , 김현수 ¹ , 김지훈 ² ¹ <i>Department of Electronics Engineering, Chungnam National University</i> , ² <i>Department of Electrical and Information Engineering, Seoul National University of Science and Technology</i> |
| WH1-O-5 09:30-09:45 | Zynq Implementation for Efficient Image Registration based on Generalized Hough Transform Jun Won Mun, Yoo Jun Nam, Yune Seok Jang, and Jae Seok Kim <i>Department of Electrical and Electronic Engineering, Yonsei University</i> |