L. Analog Design 분과

[WE4-L] Analog Circuit Design

좌장: 김종선(홍익대학교), 허승찬(삼성전자)

WE4-L-1 15:25-15:40	**A 15mV/℃ High Sensitivity Temperature Sensor with -40dB Noise Rejection** Tei Cho, Seunghyeon Park, Hyungchul Park, and Kwyro Lee *School of Electrical Engineering, KAIST*
WE4-L-2 15:40-15:55	**Design of a Nano-Ampere Current Sensing Technique for OLED Panels** Minhyun Jin, Daehyeok Kim, and Minkyu Song *Department of Semiconductor Science, Dongguk University*
WE4-L-3 15:55-16:10	**Pre-Emphasis with Capacitive Peaking Technique for 10-Gb/s Serial-Link Transmitter** Yohan Hong, Dong-Hyun Yoon, Dong-Kyu Jung, and Kwang-Hyun Baek *School of Electrical and Electronics Engineering, Chung-Ang University*
WE4-L-4 16:10-16:25	**A 4-Channel 6-Gb/s VCSEL Driver Array for Active Optical Cables** Chaerin Hong and Sung Min Park *Department of Electronics Engineering, Ewha Womans University*
WE4-L-5 16:25-16:40	**Design of DC-DC BUCK Converter with Adaptive Dead-Time Controller** Muhammad Riaz ur Rehama, Hamed Abbasizadeh, and Kang-Yoon Lee *Information and Communication Engineering, Sungkyunkwan University*
WE4-L-6 16:40-16:55	**Design of a High Speed Full-HD CMOS Image Sensor with a New Column Parallel SAR ADC** Ohhun Kwon, Daehyeok Kim, and Minkyu Song *Department of Semiconductor Science, Dongguk University*
WE4-L-7 16:55-17:10	**A 1.5-Gb/s CMOS TX and RX Chipset for Coaxial Cables with 20 dB Loss** Kyungmin Lee and Sung Min Park *Department of Electronics Engineering, Ewha Womans University*