

# 제23회 한국반도체학술대회

2016년 2월 22일(월)-24일(수), 강원도 하이원리조트

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## O. System LSI Design 분과

Room F  
봉래 I (6층)

2016년 2월 24일(수) 10:10-11:40

[WF2-O] VLSI System Design and Applications

좌장 : 김경기(대구대학교), 김지훈(서울과학기술대학교)

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| WF2-O-1 | 10:10-10:25 | <b>Advanced ZigBee Baseband Processor for Internet-of-Things Applications</b><br>Jeonghyun Cheon, Hyunsu Hwang, and Yunho Jung<br><i>School of Electronics and Information Engineering, Korea Aerospace University</i>                        |
| WF2-O-2 | 10:25-10:40 | <b>Influence of Parasitic Power/Ground Resistance in Low Voltage Device under CDM ESD Stress</b><br>Dongsin Kim, Hyunjin Shin, Youngchul Kim, Kiwon Seo, and Doyoon Kim<br><i>LDS Design, Standard Product Group, Magnachip Semiconductor</i> |
| WF2-O-3 | 10:40-10:55 | <b>Diagonally-reinforced Lane Detection Scheme for High-performance ADAS</b><br>Mingu Park, Kyoungho Yoo, and Youngjoo Lee<br><i>Department of Electronics Engineering, Kwangwoon University</i>  |
| WF2-O-4 | 10:55-11:10 | <b>Vivado HLS를 사용한 Image Fusion의 Xilinx FPGA 구현</b><br>Sangeun Ahn, Jeonghwan An, and Joonghwee Cho<br><i>Department of Embedded System Engineering, Incheon National University</i>  |
| WF2-O-5 | 11:10-11:25 | <b>Two Step Forward Backward Architecture for Non-Binary LDPC Decoder</b><br>Injun Choi and Ji-Hoon Kim<br><i>Department of Electronics Engineering, Chungnam National University</i>   |
| WF2-O-6 | 11:25-11:40 | <b>Retention-Time Aware Reconfigurable eDRAM-based FIFO Memory for Refresh-free DSP Design</b><br>Byeonggil Park, Gyuseong Kang, and Jongsun Park<br><i>School of Electrical Engineering, Korea University</i>                                |