## 제23회 한국반도체학술대회

2016년 2월 22일(월)-24일(수), 강원도 하이워리조트

#### A. Interconnect & Package 분과

Room A

태백 I (5층)

2016년 2월 24일(수) 10:00-11:40

[WA2-A] A3: Novel Interconnect and Packaging Technologies for Emerging Electronics

좌장: 권오중(인천대학교), 조태제(삼성전자)

WA2-A-1	10:10-10:25	Enhancing Fatigue Resistance of Screen-Printed Silver Nanocomposite Circuits for Stretchable Electronics Kwang-Seok Kim <sup>1</sup> , Dae Up Kim <sup>1</sup> , and Seung-Boo Jung <sup>2</sup> <sup>1</sup> Convergence Components and Agricultural Machinery Application Group, Korea Institute of Industrial Technology, <sup>2</sup> School of Advanced Materials Science and Engineering, Sungkyunkwan University
WA2 A 2	10.25 10.40	Stratchable Electronic Conductors of DEDOT-DSS Organization

#### WA2-A-2 10:25-10:40 Stretchable Electronic Conductors of PEDOT:PSS Organogel for Stretchable Electronics

Yoo-Yong Lee, Ho-Young Kang, Seok Hyeon Gwon, Gwang Mook Choi, Seung-Min Lim, Jeong-Yun Sun, and Young-Chang Joo Department of Materials Science and Engineering, Seoul National University

### WA2-A-3 10:40-10:55 Performance Enhancement for Ag Nanowire-Based Transparent Conductor Using TiO<sub>2</sub>:Cs Sol-gel

Sunho Kim<sup>1</sup>, Jun-gu Kang<sup>1</sup>, Tae-yil Eom<sup>2</sup>, and Hoo-Jeong Lee<sup>1,2</sup>
<sup>1</sup>School of Advanced Materials Science and Engineering,
SungkyunKwan University, <sup>2</sup>Sungkyunkwan Advanced Institute of
Nano Technology, Sungkyunkwan University

### WA2-A-4 10:55-11:10 Scaling Issues in Fatigue Behavior of Cu Interconnects on Flexible Substrate

Young-Joo Lee, Hae-A-Seul Shin, Han-Wool Yeon, Dae-Hyun Nam, Jung-Kwon Yang, and Young-Chang Joo Department of Materials Science and Engineering, Seoul National University

#### WA2-A-5 11:10-11:25 Fully-Packaged Ultrathin Si-based Flexible NAND Flash Memory

Do Hyun Kim, Hyeon Gyun Yoo, and Keon Jae Lee Department of Materials Science and Engineering, KAIST

# WA2-A-6 11:25-11:40 Photoluminescence Characteristics After Phosphorous Implantation into Tensile Strained Ge-on-Si for CMOS-compatible Optical Lasers

Jiwoong Baek<sup>1,2</sup>, Bugeun Ki<sup>1,2</sup>, Chulwon Lee<sup>3</sup>, Yong-Hoon Cho<sup>3</sup>, Donguk Nam<sup>4</sup>, and Jungwoo Oh<sup>1,2</sup>

<sup>1</sup>School of Integrated Technology, Yonsei University, <sup>2</sup>Yonsei Institute of Convergence Technology, <sup>3</sup>Department of Physics, KI for the NanoCentury, KAIST, <sup>4</sup>Silicon Photonics Research Laboratory, Inha

The 23<sup>rd</sup> Korean Conference on Semiconductors (KCS 2016)

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