

제23회 한국반도체학술대회

2016년 2월 22일(월)-24일(수), 강원도 하이원리조트

J. Nano-Science & Technology 분과

Room H
육백 I (6층)

2016년 2월 23일(화) 10:40-12:40

[TH2-J] Nanofabrication for Application

좌장 : 선정윤(서울대학교), 황석원(고려대학교)

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| TH2-J-1 | 10:40-10:10 | [초청] Silicon Oxide-based Memory and Three-dimensional Nanoporous System for Ultrahigh Density Storage
Gunuk Wang
<i>KU-KIST Graduate School of Converging Science and Technology, Korea University</i> |
| TH2-J-2 | 11:10-11:25 | Alternative Etching Technique of Metal-Assisted Chemical Etching for High Aspect Ratio 3D-Device Fabrication
Keorock Choi ^{1,2} and Jungwoo Oh ^{1,2}
<i>¹School of Integrated Technology, Yonsei University, ²Yonsei Institute of Convergence Technology</i> |
| TH2-J-3 | 11:25-11:40 | Three-dimensional Synthesis of ZnO Nanostructures by Polarity-selective Multistage Growth
Won Woo Lee, Seunghee H. Cho, Dong Won Yang, and Won Il Park
<i>Division of Materials Science and Engineering, Hanyang University</i> |
| TH2-J-4 | 11:40-12:10 | [초청] The Solution-based Sorting and Assembly of Single-walled Carbon Nanotubes for Nano-, Printed and Flexible Electronics
Steve Park ¹ H.-S Philip Wong ² , and Zhenan Bao ³
<i>¹Department of Materials Science and Engineering, KAIST, ²Department of Electrical Engineering, Stanford University, USA, ³Department of Chemical Engineering, Stanford University, USA</i> |
| TH2-J-5 | 12:10-12:25 | Ink-jet Printed Ambipolar Transistors and Inverters based on Semiconducting Carbon Nanotubes with Chemical Doping Technique
Juhee Lee, Bongsik Choi, Jinsu Yoon, Minsu Jeon, Yongwoo Lee, Jungmin Han, Jieun Lee, Dong Myong Kim, Dae Hwan Kim, and Sung-Jin Choi
<i>School of Electrical Engineering, Kookmin University</i> |
| TH2-J-6 | 12:25-12:40 | Alignment of Unpurified, Solution-Processed Single Walled Carbon Nanotubes via Dielectrophoresis and Its Purification Scheme
Geun Woo Baek, Yoon Gy Hong, In Jae Yim, Hyeun Woo Kim, You-Beom Lee, Jae Hyeon Ryu, Gi Taek Yu, and Sung Hun Jin
<i>Department of Electronic Engineering, Incheon National University</i> |