

제23회 한국반도체학술대회

2016년 2월 22일(월)-24일(수), 강원도 하이원리조트

O. System LSI Design 분과

Room F
봉래 I (6층)

2016년 2월 23일(화) 10:40-12:40

[TF2-O] VLSI System Design for Communications

좌장 : 박종선(고려대학교), 이영주(광운대학교)

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| TF2-O-1 | 10:40-10:55 | Cost-Effective and High-Speed Baseband Modem for Gigabit Proximity Wireless Communications
Ik-Jae Chun, Hoo-Sung Lee, and Moon-Sik Lee
<i>Wireless Application Research Department, Electronics and Telecommunications Research Institute</i> |
| TF2-O-2 | 10:55-11:10 | Unnecessary Pseudo-Read Prevent SRAM for Low-Power Viterbi Decoder
Woong Choi, Kyungho Shin, and Jongsun Park
<i>School of Electrical Engineering, Korea University</i> |
| TF2-O-3 | 11:10-11:25 | Low-latency Error-detection and Synchronization Architecture for High-speed RFID-based Train Protection Systems
Yunho Park and Youngjoo Lee
<i>Department of Electronics Engineering, Kwangwoon University</i> |
| TF2-O-4 | 11:25-11:40 | A Low Complexity Massive MIMO Detection Architecture based on Richardson Iterative Method
Dongyeob Shin, Ji-Hwan Yoon, Gihoon Jung, and Jongsun Park
<i>School of Electrical Engineering, Korea University</i> |
| TF2-O-5 | 11:40-11:55 | A Simple and Fast Giga Link between a Host and a FPGA
Hoo-Sung Lee ^{1,2} , Ik-Jae Chun ¹ , Moon-Sik Lee ¹ , and Ji-Hoon Kim ²
<i>¹Wireless Application Research Department, Electronics and Telecommunications Research Institute, ²Department of Electronics Engineering, Chungnam National University</i> |
| TF2-O-6 | 11:55-12:10 | FPGA Implementation of Quantized Gaussian Noise Channel for Error-Control System Verification
GilSu Yun ¹ , Youngjoo Lee ² , and Jeongseok Ha ¹
<i>¹School of Electrical Engineering, KAIST, ²Department of Electronic Engineering, Kwangwoon University</i> |