

제23회 한국반도체학술대회

2016년 2월 22일(월)-24일(수), 강원도 하이원리조트

L. Analog Design 분과

Room A
태백 I (5층)

2016년 2월 23일(화) 10:40-12:40

[TA2-L] Analog Design II

좌장 : 박재진(삼성전자)

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| TA2-L-1 | 10:40-10:55 | A 1.2 V CMOS-based Temperature Sensor in the Subthreshold Operation
Woosul Shin, Jun-Seok Na, Bong-Choon Kwak, Seong-Kwan Hong, and Oh-Kyong Kwon
<i>Department of Electronic Engineering, Hanyang University</i> |
| TA2-L-2 | 10:55-11:10 | SIDO DC-DC 컨버터의 부하 범위 확장을 위한 cross regulation 감소 방법
정현수, 홍요한, 팜응옥손, 백광현
<i>중앙대학교 전자전기공학과</i> |
| TA2-L-3 | 11:10-11:25 | Fast-transient Output-capacitorless LDO Regulator for SoC Applications
Eun-Taek Sung, Jeong-Yun Lee, Keum-Won Ha, Ye-Seul Baek, and Donghyun Baek
<i>School of Electrical Engineering, Chung-Ang University</i> |
| TA2-L-5 | 11:40-11:55 | Design of a Transceiver Transmitting Power, Clock, and Data over a Single Optical Fiber for Future Automotive Network System
Woorham Bae and Deog-Kyoon Jeong
<i>Department of Electrical and Computer Engineering, Seoul National University</i> |
| TA2-L-6 | 11:55-12:10 | A 3/6/12-Gb/s Multi-Rate Clock and Data Recovery Circuit with a Multi-Mode Rotational Bang-Bang Phase Detector
Ki-Hyun Pyun, Dae-Hyun Kwon, and Woo-Young Choi
<i>Department of Electrical and Electronic Engineering, Yonsei University</i> |