

# 제22회 한국반도체학술대회

2015년 2월 10일(화) - 12일(목), 인천 송도컨벤시아

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## J. Nano-Science & Technology 분과

Room I

1F / 111호

2015년 2월 11일(수) 13:00-14:30

[WI2-J] 메모리 소자, 나노 융합/유연 소자

좌장: 박철민 (연세대학교), 이택희 (연세대학교)

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| WI2-J-1 | 13:00-13:30 | <b>[초청] Inkjet-Printed Stretchable Electrodes based on Nano-Particle Materials</b><br>Yongtaek Hong, Sangwoo Kim, Taehoon Kim, Junghwan Byun, Seongdae Choi, Seungjun Chung, and Jaemyeon Lee<br>Department of Electrical and Computer Engineering, Seoul National University   |
| WI2-J-2 | 13:30-14:00 | <b>[초청] Resistive Memory and Switch Device for Stackable Scalable 3D Nanoscale Memory</b><br>Myoung-Jae Lee <sup>1</sup> , Ji-Hoon Ahn <sup>1</sup> , Ji Ho Sung <sup>1,2</sup> , Hoseok Heo <sup>1,2</sup> , Kyungwook Kim <sup>1,3</sup> , and Moon-Ho Jo <sup>1,2,3</sup><br><sup>1</sup> Center for Artificial Low Dimensional Electronic Systems, Institute for Basic Science (IBS), <sup>2</sup> Division of Advanced Materials Science, POSTECH, <sup>3</sup> Department of Materials Science and Engineering, POSTECH |
| WI2-J-3 | 14:00-14:15 | <b>Conductive Filaments Control of Resistive Memories Employing Self-Assembled Silica-Nanodots for Switching Stability Improvement</b><br>Byoung Kuk You, Jong Min Kim, Yeon Sik Jung, and Keon Jae Lee<br>Department of Materials Science and Engineering, KAIST   |
| WI2-J-4 | 14:15-14:30 | <b>Transferred Partially-Grown Patterned Graphene Layer for Charge-Floating Gate in Organic Non-Volatile Memory transistors</b><br>Yunhwan Park <sup>1</sup> , Subeom Park <sup>2</sup> , Byung Hee Hong <sup>2</sup> , and Yongtaek Hong <sup>1</sup><br><sup>1</sup> Department of Electrical Engineering and Computer Science, Inter University Semiconductor Research Center (ISRC,) Seoul National University, <sup>2</sup> Department of Chemistry Seoul National University  |