

제22회 한국반도체학술대회

2015년 2월 10일(화) - 12일(목), 인천 송도컨벤시아

O. System LSI Design 분과

Room G
1F / 109호

2015년 2월 11일(수) 09:00-10:30

[WG1-O] Systems and Applications

좌장: 김지훈 (충남대학교), 이한호 (인하대학교)

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| WG1-O-1 | 09:00-09:15 | Area-Efficient and High-Throughput Architecture for Galois/Counter Mode (GCM) Hoo-Sung Lee ^{1,2} , Ik-Jae Chun ¹ , Moon-Sik Lee ¹ , and Ji-Hoon Kim ² ¹ Wireless Proximity Communications Research Section, ETRI, ² Division of Electrical and Information Technology Engineering, Chungnam National University |
| WG1-O-2 | 09:15-09:30 | Efficient Successive-Cancellation Polar Decoder 윤하람, 이한호 인하대학교 정보통신공학과 |
| WG1-O-3 | 09:30-09:45 | FPGA Implementation of a Barrel and Vignetting Distortion Correction Processor for Wide-Angle Cameras Won-Tae Kim, Hui-Sung Jeong, Jinu Hong, Sunah Moon, and Tae-Hwan Kim School of Electronics, Telecommunication & Computer Engineering, Korea Aerospace University |
| WG1-O-4 | 09:45-10:00 | 멀티스레드 효율을 개선한 Multi-banked Cache 설계 Gyutaek Kyung, Dohyun Kim, and Kwangyeob Lee Department of Computer engineering, Seokyeong University |
| WG1-O-5 | 10:00-10:15 | An Area Efficient Scaling-Free CORDIC Algorithm Using Global Shifting Sum (GSS) Method Gihoon Jung and Jongsun Park Department of Electrical Electronic Engineering, Korea University |
| WG1-O-6 | 10:15-10:30 | A MP-based BCH Encoder for Reducing the Latency of SC based on re-Encoding Architecture Hoyoung Tang and Jongsun Park School of Electrical Engineering, Korea University |