

제22회 한국반도체학술대회

2015년 2월 10일(화) - 12일(목), 인천 송도컨벤시아

L. Analog Design 분과

Room B
1F / 105호

2015년 2월 12일(목) 14:50-16:20

[TB3-L] Analog Design

좌장: 백광현 (중앙대학교), 박재진 (삼성전자)

TB3-L-1	14:50-15:05	Multi-Channel Incremental Delta-Sigma ADC for Multi-Sensor Application Eun-Taek Sung, Sangyong Park, and DonghyunBaek School of Electrical Engineering, Chung-Ang University
TB3-L-2	15:05-15:20	A 10-bit Single-Ended SAR ADC for Multiple-Channel Neural Recording System Seong-Jin Cho, Ju Eon Kim, Dong Ho Shin, Hong Tae Jeon, and Kwang-Hyun Baek School of Electrical and Electronics Engineering, Chung-Ang University
TB3-L-3	15:20-15:35	A 3.8 MHz Wien-Bridge Oscillator with Capacitive Automatic Amplitude Control in 0.35μm CMOS Hyeongyeol Park ¹ , Seunghyun Kim ¹ , Taemin Kim ² , and Hyunchol Shin ¹ ¹ Kwangwoon University, ² Now with LIG Nex ¹ Communication Research Center
TB3-L-4	15:35-15:50	A 12b 1.25MS/s SAR ADC Using Dual Sampling Technique and Low Switching Energy Technique In-Su Cho, Min-Ki Kim, Sung-Chan Rho, and Shin-Il Lim Department of Electronics Engineering, Seokyeong University
TB3-L-5	15:50-16:05	A 0.6V 1.17ps PVT Tolerant and Synthesizable Time-to-Digital Converter using a Stochastic Phase Interpolation in 14nm FinFET Technology Sung-Jin Kim, Taeik Kim, Jaejin Park, and Hojin Park Department of System SLI, Samsung Electronics
TB3-L-6	16:05-16:20	변이 적응성과 에너지 효율성을 갖는 NEM(Nano ElectroMechanical) 릴레이 전하펌프 변상돈, 모현선, 신상학, Son Ngoc Truong, 송재상, 민경식 국민대학교 전자공학부