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Date	Feb. 26, 2014 (Wed.)
Place	Room I / 제1공학관 408호 (# 408, Engineering Building I)

WP2-1 **Influence of Gate Dielectrics on Electrical Characteristics of Solution-Processed ZnO Transistors**
저자: Xue Zhang[1], Jaehoon Park[1], Hyunji Shin[2], Dong Wook Kim[2], Jong Sun Choi[2], Jae Eun Hwang[3], and Hong Doo Kim[3]
소속: [1]Department of Electronic Engineering, Hallym University, [2]Department of Electrical, Information and Control Engineering, Hongik University, [3]Deparment of Display Materials Engineering, Kyunghee University

WP2-2 **Purge-Time-Induced Changes in Preferred Orientation of Zinc Oxide Thin Films Grown by Atomic Layer Deposition**
저자: Hui Kyung Park and Jaeyeong Heo
소속: Department of Materials Science and Engineering, Chonnam National University

WP2-3 **Flexible Micro-Scale Organic Field Effect Transistors Fabricated Achieved via Orthogonal Photolithography**
저자: Jingon Jang[1], Younggul Song[1], Hyuntaek Oh[2], Daekyoung Yoo[1], Jin-Kyun Lee[2], and Takhee Lee[1]
소속: [1]Department of Physics and Astronomy, Seoul National University, [2]Department of Polymer Science and Engineering, Inha University

WP2-4 **Gate Dielectric Effects on Electrical Characteristics of 6,13-Bis(Triisopropylsilylethynyl)-Pentacene Transistors**
저자: Hyunji Shin[1], Dongwook Kim[1], Jaehoon Park[2], and Jong sun Choi[1]
소속: [1] Department of Electrical, Information and Control Engineering, Hongik University, [2] Department of Electronic Engineering, Hallym University

WP2-5 **Deposition of Thicker Ferroelectric (Hf,Zr)O_2 Thin Films using Al_2O_3 Inter-Layer**
저자: Han Joon Kim, Min Hyuk Park, Yu Jin Kim, Taehwan Moon, and Cheol Seong Hwang
소속: Department of Material Science & Engineering and Inter-university Semiconductor Research Center, Seoul National University

WP2-6 **Improving Conformality of $SrRuO_3$ Film Grown by Combined ALD SrO and CVD RuO_2 or Ru Layers**
저자: Cheol Hyun An, Woojin Jeon, Woongkyu Lee, Yeon Woo Yoo, and Cheol Seong Hwang
소속: Department of Materials Science and Engineering and Inter-university Semiconductor Research Center, Seoul National University

WP2-7 **Microwave-Annealing Effects of Solution-Processed HfO_x Thin Film as a Resistive Switching for ReRAM**
저자: Ki-Hyun Jang, Se-Man Oh, Se-Ho Kim, and Won-Ju Cho
소속: Department of Electronic Materials Engineering, Kwangwoon University

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Date	Feb. 26, 2014 (Wed.)
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WP2-8 **Surface Reaction Chemistry during Atomic Layer Deposition of Sc_2O_3 and Gd_2O_3 from Cp-Based Metal Precursors**
저자: Jeong Hwan Han[1,2]
소속: [1]Present address: Advanced Materials Division, Korea Research Institute of Chemical Technology, [2]Imec

WP2-9 **Characterization of Charge Trapping and Current Conduction Mechanism in Hf-Aluminate for 3D-Stacked NAND Flash Memory**
저자: Jinho Oh, Heedo Na, and Hyunchul Sohn
소속: Department of Materials Science and Engineering, Yonsei University

WP2-10 **A Novel Low-Temperature Treatment on Solution-Derived Amorphous InGaZnO Thin-Film Transistor for Flexible Display**
저자: Yeong-Hyeon Hwang, Sung-Wan Moon, Ja-Kyung Gu, and Won-Ju Cho
소속: Department of Electronic Materials Engineering, Kwangwoon University

WP2-11 **The Speed Improvement of In_3SbTe_2 Phase Change Material by Doping Bi Element**
저자: Minho Choi[1], Yong Tae Kim[2], and Jinho Ahn[1]
소속: [1]Department of Materials science and engineering, Hanyang University, [2]Semiconductor Materials and Device Laboratory, Korea Institute of Science and Technology

WP2-12 **High Performance Solution-Processed MoS_2 Field Effect Transistor by Two-Step Annealing**
저자: Juyeon Won, Chul-Kyu Lee, Byeong-Geun Son, Hyo Jin Kim, Soyeon Je, and Jae-Kyeong Jeong
소속: Department of Materials Science and Engineering, Inha University

WP2-13 **Solution Processed CuO_x and Its Transport Characteristics**
저자: Si-Hong Kim, Myung-Ji Kim, Ji-Su Ahn, and Deok-Kee Kim
소속: Department of Electrical Engineering, Sejong University

WP2-14 **Improvement in Bias Stability of Amorphous IGZO Thin Film Transistors by High Pressure H_2O_2 Annealing**
저자: Ji Hun Song, Chang-Kyu Lee, Ah Young Hwang, and Jae Kyeong Jeong
소속: Department of Materials Science and Engineering, Inha University

WP2-15 **Soluble-Processed Zr-La-O/SiO_2 Gate Dielectrics at 180℃ for Flexible Metal Oxide Transistors**
저자: Soyeon Je, Byeong-Guen Son, Hyojin Kim, Juyeon Won, and Jaekyeong Jeong
소속: Department of Materials Science and Engineering, Inha University

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Date	Feb. 26, 2014 (Wed.)
Place	Room I / 제1공학관 408호 (# 408, Engineering Building I)

WP2-16 **Atomic Layer Deposition of Ruthenium Thin Film from Ru Precursor**
저자: Hyo Jun Jung[1,2], Eun Ae Jung[1], Jeong Hwan Han[1], Bo Keun Park[1], Sun Sook Lee[1], Jin Ha Hwang[2], Chang Gyoun Kim[1], Ki Seok An[1]. and Taek Mo Chung[1]
소속: [1]Advanced Materials Division, Korea Research Institute of Chemical Technology, [2]Department of Materials Science and Engineering, Hongik University

WP2-17 **Anomalous Behavior of Oxygen Gas Ratio-Dependent Field Effect Mobility in In-Zn-Sn-O Thin Film Transistor**
저자: Ah Young Hwang, Chang-Kyu Lee, Ji Hun Song, and Jae Kyeong Jeong
소속: Department of Materials Science and Engineering, Inha University

WP2-18 **Effect of HfO_2 Charge Trap Layer and Al_2O_3 Blocking Layer Thickness on MAHAS Structure Memory Characteristics**
저자: Heedo Na, Jinho Oh, Kyumin Lee, and Hyunchul Sohn
소속: Department of Material Science and Engineering, Yonsei University

WP2-19 **The Effects of Post Annealing on the Schottky Behaviors of Atomic Layer Deposited Ruthenium on the Si Substrate**
저자: Donghwan Lim[1], Moon-Suk Choi[1], Dohyung Kim[1], Youngil Gil[1], Woosuk Jung[1], Seong Chan Heo[2], and Changhwan Choi[1]
소속: [1] Division of Materials Science and Engineering, Hanyang University, [2] SK hynix Semiconductor

WP2-20 **Top-Gate Oxide Thin-Film Transistors using Solution-Processed Gate Stack of PVP/Al-Zn-Sn-O with an Al_2O_3 Capping Layer for Full-Patterning Process**
저자: Kyeong-Ah Kim, Jun-Yong Bak, and Sung-Min Yoon
소속: Department of Advanced Materials Engineering for Information & Electronics, Kyung Hee University

WP2-21 **Improvements in Bias-Stress Stability Characteristics of Solution-Processed Al-In-Zn-O Thin-Film Transistors with Optimizing the Channel Composition**
저자: Minji Park, Jun-Yong Bak, Jeong-Seon Choi, and Sung-Min Yoon
소속: Department of Advanced Materials Engineering for Information and Electronics, Kyung Hee University

WP2-22 **Thickness and Composition Effects of Al-Doped ZnO Channels Prepared by Atomic Layer Deposition on the Device Behaviors of Oxide Thin-Film Transistors**
저자: Eomji Kim, Jun-Yong Bak, Jeong-Seon Choi, and Sung-Min Yoon
소속: Department of Advanced Materials Engineering for information and Electronics of Engineering, Kyung Hee University

WP2-23 **Effect of Bottom Gate Insulator Thickness on the Threshold-Voltage Tunability and Stress Stabilities of the Fully-Transparent Double-Gate In-Ga-Zn-O TFTs**
저자: Da-Bin Jeon and Sung-Min Yoon
소속: Department of Advanced Materials Engineering for Information and Electronics, Kyung -Hee University

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Date	Feb. 26, 2014 (Wed.)
Place	Room I / 제1공학관 408호 (# 408, Engineering Building I)

WP2-24 **Effects of Ferrite Core Loss and Permeability at 400 kHz Ferrite Inductively Coupled Plasma**
저자: Sung-Won Cho[1], June Young Kim[2], and Chin-Wook Chung[1]
소속: [1]Department of Electrical Engineering, Hanyang University, [2]Department of Nanoscale Semiconductor Engineering, Hanyang University

WP2-25 **In-Situ Measurement Method of Dielectric-Film Thickness for Processing Chamber Wall Monitoring**
저자: Jin-Yong Kim and Chin-Wook Chung
소속: Department of Electrical Engineering Hanyang University

WP2-26 **Comparison of Properties of Poly SiGe Deposited by Disilane and Trisilane**
저자: Hyunchul Jang, Sangmo Koo, Byongju Kim, Sun-Wook Kim, and Dae-Hong Ko
소속: Department of Materials Science and Engineering, Yonsei University

WP2-27 **Bipolar Resistive Switching in Amorphous SrTiO3 Films Grown by Atomic Layer Deposition**
저자: Woongkyu Lee, Kyung Jean Yoon, Woojin Jeon, Yeon Woo Yoo, Cheol Hyun An, and Cheol Seong Hwang
소속: Department of Materials Science and Engineering and Inter-university Semiconductor Research Center, Seoul National University

WP2-28 **Distribution of Plasma Parameters at Wafer Level Measured by 2D Plasma Parameter Diagnostic Method in Inductively Coupled Plasmas**
저자: Young-Cheol Kim[1], and Chin-Wook Chung[1]
소속: [1]Department of Nanoscale Semiconductor Engineering, Hanyang University

WP2-29 **Characteristics of Grain Boundary and Interface Traps in Polysilicon Channel Thin Film Transistors**
저자: Yoonseok Jeon, Musarrat Hasan, Cuong Nguyen Manh, Seung-Won You, Duc-Tai Tong, Dong-Hwi Lee, Jae-Kyung Jeong and Rino Choi
소속: Inha University

WP2-30 **Stress Induced Leakage Current Characteristic of La-Incorporated HfO2 Gate Dielectric**
저자: Seung-won You, Musarrat Hasan, Manh Cuong Nguyen, Yoon Seok Jeon, Duc-Tai Tong, Dong-Hwi Lee, Jae Kyoung Jung and Rino Choi
소속: Department of Materials Science and Engineering, Inha University

WP2-31 **A Study on the Enlargement of the Plasma Reactor using a Global Model**
저자: Dong-Hwan Kim[1], Young-Kwang Lee[2], and Chin-Wook Chung[2]
소속: [1]Department of Nanoscale Semiconductor Engineering, Hanyang University, [2]Department of Electrical Engineering, Hanyang University

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Date	Feb. 26, 2014 (Wed.)
Place	Room I / 제1공학관 408호 (# 408, Engineering Building I)

WP2-32 **Conduction Mechanism of Metal-Oxide-Semiconductor Field Effect Transistor with La – Incorporated Hf Based Dielectric**
저자: Dong-Hwi Lee, Musarrat Hasan, Manh-Cuong Nguyen, Yoon-Seok Jeon, Seung-Won You, Duc-Tai Tong, Jae-Kyoung Jeong, and Rino Choi
소속: School of Materials Science and Engineering Inha University

WP2-33 **Optimization of GZO/Ag/GZO Multilayer Electrodes Obtained by Pulsed Laser Deposition at Room Temperature**
저자: Eunyoung Cheon, Sang Woo Song, Hwan Sun Kim, Dae Hui Cho, Ji Hun Jang, and Byung Moo Moon
소속: Department of Electrical Engineering, Korea University

WP2-34 **Characteristics of Solution Based Oxide TFT with Solution Heating**
저자: Sang-A Oh[1], Kyeong Min Yu[1], So-Hyun Jeong[1], Eui-Jung Yun[2], and Byung-Seong Bae[1]
소속: [1]Department of Display Engineering, Hoseo University, [2]College of IT Engineering and System Control Engineering, Hoseo University

WP2-35 **A Study of Advanced Al RDL Development**
저자: Koeun Cheon, Yonji Park, Yongkuk Kim, Sungwon Yoon, Sangjae Kim, Minki Son, Junhyun Cho, Pilsoon Bae, Jaesung Oh, Jaemyun Kim, and Kwangyoo Byun
소속: SK hynix

WP2-36 **Effect of ALD Grown Aluminum Oxide Film on the IGZO TFT**
저자: Heeok Kim[1], Jong-Heon Yang, Sung Haeng Cho, Minki Ryu, Jae-Eun Pi, Jong-Woo Kim, Oh Sang Kwon, Eun Suk Park, Chi-Sun Hwang, and Sang-Hee Ko Park
소속: Oxide TFT Research Section, Electronics and Telecommunications Research Institute

WP2-37 **Suppression of Current Collapse Effect by Insertion of Mo on Ni/Au Based Schottky Contacts in AlGaN/GaN HEMT**
저자: Su-Keun Eom, Neung-Hee Lee, and Kwang-Seok Seo
소속: Department of Electrical and Computer Engineering, Seoul National University

WP2-38 **Improvement of Device Characteristic on Solution-Processed InGaZnO Pseudo Metal-Oxide-Semiconductor Field-Effect-Transistor using Microwave Annealing.**
저자: Sung-Wan Moon, Ja-Gyeong Gu, and Won-Ju Cho
소속: Department of Electronic Materials Engineering, Kwangwoon University

WP2-39 **Effect of Baking Temperature on Device Characteristics in TFT based Solution Processed Amorphous ZnSnO**
저자: Kwang-Won Cho, Young-Hyun Hwang, and Won-Ju Cho
소속: Department of Electrical Material Engineering, Kwangwoon University

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Date	Feb. 26, 2014 (Wed.)
Place	Room I / 제1공학관 408호 (# 408, Engineering Building I)

WP2-40 **선택적 리세스 게이트 소자의 공정 및 특성 분석**
저자: 김정진[1,2], 박영락[1], 고상춘[1], 문재경[1], 장우진[1], 장우영[1], 배성범[1], 양전욱[2]
소속: [1]한국전자통신연구원, [2]전북대학교 반도체화학공학부

WP2-41 **Electrical Characteristics and Instability of Solution-Derived An-Zn-Sn-O Thin-Film Transistors**
저자: Yeong-Hyeon Hwang and Won-Ju Cho
소속: Department of Electronic Materials Engineering, Kwangwoon University

WP2-42 **Systematic Analysis of Electrical Traps at Surface, AlGaN Barrier, and GaN Buffer of AlGaN/GaN HFET Device**
저자: Seung Yup Jang[1], Jong-Hoon Shin[1], Myeong-Kyu Eo[2], Hyo-Seung Choi[2], Hyuck-In Kwon[2], and TaeHoon Jang[1]
소속: [1]IGBT part, System IC R&D Laboratory, LG Electronics, [2]School of Electronic and Electrical Engineering, Chung-Ang University

WP2-43 **RF Characteristics of GaN on SiC for Different Device Topology**
저자: Youngrak Park, Woojin Jang, Sangchoon Ko, Jungjin Kim, Wooyoung Jang, Sungbum Bae, Jongwon Lim, Hogyun Ahn, and Jaekyung Mun
소속: GaN Power Electronics Research Section, Electronics and Telecommunications Research Institute

WP2-44 **에피텍셜 방법으로 성장된 In-Situ SiN 의 Surface Trap 감소 효과 연구**
저자: 조성무, 황의진, 김재무, 김준호, 장태훈
소속: LG전자 System, IC 연구소 IGBT Part

WP2-45 **Structural Optimization of Field-Plated Normally-off AlGaN/GaN-on-Si MOSHFETs**
저자: Jae-Gil Lee, Bong-Ryeol Park, and Ho-Young Cha
소속: School of Electronic and Electrical Engineering, Hongik University

WP2-46 **Effect of Basal-Plane Stacking Faults on X-Ray Diffraction of Nonpolar a-Plane GaN Films**
저자: Ji Hoon Kim[1], Sung-Min Hwang[2], Kwang Hyeon Baik[3], and Jung Ho Park[1]
소속: [1]School of Electrical Engineering, Korea University, [2]Photonics Convergence Research Center, Korea Electronics Technology Institute, [3]Department of Materials Science and Engineering, Hongik University

WP2-47 **Ammonium Polysulfide Passivation for Interface between GaN and Atomic-Layer-Deposited HfAlOx**
저자: Donghwan Lim, Woosuk Jung, and Changhwan Choi
소속: Division of Materials Science & Engineering, Hanyang University

WP2-48 **A Study on Crystalline ZnTe Channel Characteristics for Thin Film Transistor Device**
저자: Yoonki Min, Jonggi Kim, Heedo Na, and Hyunchul Sohn
소속: Department of Materials Science & Engineering, Yonsei University

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Date	Feb. 26, 2014 (Wed.)
Place	Room I / 제1공학관 408호 (# 408, Engineering Building I)

WP2-49 **Study of N-contact Hole Number of Via Hole Vertical LEDs for Electrical and Optical Properties**
저자: Chi Gyun Song[1], Hyung-Jo Park[2], Tak Jeong[2], Sang Hern Lee[2], and Joon Seop Kwak[1]
소속: [1]Department of Printed Electronics Engineering, Sunchon National University, [2]LED Device Team, Korea Photonics Technology Institute

WP2-50 **Growth of Semi-Insulating C-Doped/Undoped GaN Multiple-Layer Buffer**
저자: Chul-Ho Won, Sang-Min Jeon, Dong-Seok Kim, Soo-Jin Yu, Hee-Sung Kang, Young-Woo Jo, Do-Kywn Kim, Ryun-Hwi Kim, Dong-Hyeok Son, You-Mi Kwon, Vodapally Sindhuri, Jun-Hyeok Lee, Ji-Hyun Kim, Young-Jo Kim, and Jung-Hee Lee
소속: School of Electronics Engineering, Kyungpook National University

WP2-51 **Suppression of Current Collapse in AlGaN/GaN MISHFET with a Novel Buffer Structure**
저자: Young-Jo Kim, Hee-Sung Kang, Dong-Seok Kim, Young-Woo Jo, Do-Kywn Kim, You-Mi Kwon, Dong-Hyeok Son, Ji-Hyun Kim, Jun-Hyeok Lee, Young Jun Yoon, Yong Soo Lee, and Jung-Hee Lee
소속: School of Electronics Engineering, Kyungpook National University

WP2-52 **Characteristics of AlGaN/GaN HEMTs on SiC with Pt-Based Schottky Contacts**
저자: Hyung Sup Yoon, Byoung Gue Min, Ho Kyun Ahn, Jong Min Lee, Dong Min Kang, Sung Il Kim, Chul Won Ju, Hae Cheon Kim, and Jong Won Lim
소속: RF Convergence Components Research Section, IT Components & Materials Research Laboratory, Electronics and Telecommunications Research Institute

WP2-53 **Bias-dependent Characteristics of AlGaN/GaN HEMTs on SiC with T-Shaped Gate of 0.25 um Gate Length**
저자: Jong-Min Lee, Byoung-Gue Min, Hyung Sup Yoon, Ho-Kyun Ahn, Dong Min Kang, Seong Il Kim, Sang-Heung Lee, Chul Won Ju, Hae Cheon Kim, and Jong-Won Lim
소속: RF Convergence Components Research Section, IT Materials and Components Lab., Electronics and Telecommunications Research Institute

WP2-54 **A Study on the Scalability of a Threshold Type Cell Select Device using Amorphous GeSe, and the Experimental Ways for Reduction of Threshold Voltage**
저자: Hyung-Woo Ahn, Suyoun Lee, Doo Seok Jeong, Sang-Yeol Shin, and Byung-ki Cheong
소속: Electronic Materials Research Center, Korea Institute of Science and Technology

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Date	Feb. 26, 2014 (Wed.)
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WP2-55 **Dependence of Output Power Density on Gate width of AlGaN/GaN HEMT on SiC Substrate at 9.3GHz**
저자: Byoung-Gue Min, Hyung Sup Yoon, Ho-Kyun Ahn, Seong-Il Kim, Jong-Min Lee, Haecheon Kim, Dong-Min Kang, Chul-Won Ju, Sang-Heung Lee, and Jong-Won Lim

소속: RF Convergence Components Research Section, IT Components & Materials Research Laboratory, Electronics and Telecommunications Research Institute

WP2-56 **RF Performance of 13 nm-thick AlGaN/GaN HEMT with Thin Al2O3 Surface Protection Layer**
저자: Jun-Hyeok Lee[1], Ryun-Hwi Kim[1], Do-Kywn Kim[1], Chul-Ho Won[1], Ji-Hyun Kim[1], Young-Jo Kim[1]
Bok-Hyung Lee[2], Byeong-Ok Lim[2], Gil-Wong Choi[2], In-Pyo Hong[3], and Jung-Hee Lee[1]
소속: [1]School of Electronics Engineering, Kyungpook National University, [2]Samsung Thales Co., Ltd, Korea, [3]Agency for Defense Development

WP2-57 **Ultra-Violet Sensitivity of n-ZnO/p-GaN Hetero-junction Diode**
저자: Seong Gook Cho, Woong Tak Moon, and Eun Kyu Kim

소속: Department of Physics and Research Institute for Natural Sciences, Hanyang University,

WP2-58 **Photoluminescence Anisotropy in InP Quantum Dot Strings**
저자: Yongmin Kim[1], Yong Ho Shin[1], and Jindong Song[2]
소속: [1]Department of Applied Physics and Institute of Nanoscience and Biotechnology, Dankook University

WP2-59 **Optical Study of Non-polar a-Axis ZnO Single Crystal for Light Emitting Applications**
저자: Younghun Hwang and Youngho Um
소속: Department of Physics, University of Ulsan

WP2-60 **Unusual Photoluminescence Peak Shift of InSb Epitaxial Layers Grown by LP-MOCVD**
저자: Jinwook Jung[1], Sehun Park[1], Chulkyun Seok[1], Yongjo Park[2], Xiren Chen[3], Jun Shao[3], and Euijoon Yoon[1]
소속: [1]Department of Materials Science and Engineering, Seoul National University, [2]Energy Semiconductor Research Center, Advanced Institutes of Convergence Technology, Seoul National University, [3]Shanghai Institute of Technical Physics, Chinese Academy of Sciences

WP2-61 **A Design of Transceiver for Advanced UHF Band RFID Reader**
저자: Hyo-Bin Jung, Won-Jae Jung, Sang-Kyu Kim, Se-Mi Lim, Ji-Hoon Lee, Kyu-Hyun Nam, and Jun-Seok Park
소속: School of Electrical Engineering, Kookmin University

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WP2-62 **A Pipelined Digital Predistorter using CORDIC Processor**
저자: Jong Kang Park, Kyunghoon Kim, Youngoo Yang, and Jong Tae Kim
소속: School of Electronic and Electrical Engineering, Sungkyunkwan University

WP2-63 **Spectroscopic Ellipsometer를 이용한 CVD Graphene 의 광학특성 평가**
저자: 손우식[1], 문정훈[2], 현문섭[1], 양준모[1], 조병진[2]
소속: [1]나노종합기술원 특성평가사업실, [2]한국과학기술원 전기전자공학과

WP2-64 **Monte Carlo Simulation of Scanning Electron Microscopic Images of Specimens for Structural and Compositional Analysis**
저자: Myeong Chun Song and Jin Seung Kim
소속: Department of Nano Science and Technology, Graduate School, Chonbuk National University

WP2-65 **Electronic Structure of Graphene: EELS and DFT Calculation**
저자: Yun Chang Park and Jun-Mo Yang
소속: National NanoFab Center

WP2-66 **유성펜을 이용한 FIB 손상 방지용 보호층 증착 및 특성**
저자: 박윤창[1], 박병천[2], Sergey Romankov[3], 박경진[1], 유정호[1], 이용복[1], 양준모[1]
소속: [1]나노종합기술원, [2]한국표준과학연구원, [3]전북대학교 신소재공학과

WP2-67 **집속이온빔(FIB)을 이용한 GaN 계 LED 의 3 차원 전위분석**
저자: 박경진, 곽상희, 유정호, 박윤창, 양준모
소속: 나노종합기술원 특성평가실

WP2-68 **전계방출 전자빔의 전자광학계 정렬기술 연구**
저자: 최성웅[1], 이영복[1], 김대욱[1], 오태식[1], 김영정[2], 김호섭[1]
소속: [1]선문대학교 나노과학과, [2]선문대학교 신소재공학과

WP2-69 **회절광 현미경을 이용한 극자외선 마스크의 이미징 성능측정**
저자: 이재욱[1], 홍성철[1], 이승민[1], 김종석[2], 정시준[3], 김정식[4], 안진호[1,2,3,4]
소속: [1]한양대학교 신소재공학과, [2]한양대학교 정보디스플레이공학과, [3]한양대학교 나노융합과학과, [4]한양대학교 나노반도체공학과

WP2-70 **GPA를 이용한 Strained Silicone의 응력분포 해석에 대한 시편제작 방법의 영향**
저자: 이용복, 박윤창, 유정호, 박경진, 이완규, 양준모
소속: 나노종합기술원 특성평가실

WP2-71 **고속 검사를 위한 멀티전자빔 검사장비 연구**
저자: 이승범[1], 정원영[1], 조현우[1], 이준호[1], 이영복[2], 최성웅[2], 김대욱[2], 오태식[2], 김호섭[2]
소속: [1]LIG 에이디피, [2]선문대학교 나노과학과

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Date	Feb. 26, 2014 (Wed.)
Place	Room I / 제1공학관 408호 (# 408, Engineering Building I)

WP2-72 **대면적 스캔을 위한 Quadrupole Einzel Lens 구조 연구**
저자: 이영복, 김대욱, 오태식, 김호섭
소속: 선문대학교 나노과학과

WP2-73 **멀티 전자칼럼 제어방식에 대한 연구**
저자: 이순용[1,2], 임선종[1], 김호섭[2]
소속: [1]한국기계연구원 광응용기계연구실, [2]선문대학교 나노과학과