

The 21st Korean Conference on Semiconductors
제21회 한국반도체학술대회
February 24–26, 2014 / Hanyang University, Seoul, Korea

J. Nano-Science & Technology 분과

[WL1-J] 나노구조

Date	Feb. 26, 2014 (Wed.)
Place	Room L / 제1공학관 503호 (# 503, Engineering Building I)

Session Chair: 김웅 교수(고려대학교), 장호원 교수(서울대학교)

- WL1-J-1 10:50-11:20 Solution-Based Synthesis of Anisotropic Metal Chalcogenide Nanomaterials and the Challenges**
저자: Unyong Jeong
소속: Department of Materials Science and Engineering, Yonsei University
- WL1-J-2 11:20-11:35 Nano-Imprinted Metal Electrode by Solution-Based Ag Nano Particles with Methanol Capillary Force Effect**
저자: Youngin Gil, DoHyung Kim, and Changhwan Choi
소속: Division of Materials Science & Engineering, Hanyang University
- WL1-J-3 11:35-11:50 The Relationship between Adsorption Thickness of Polymer Layer on Ceria and Dishing in Shallow Trench Isolation Chemical Mechanical Planarization**
저자: Kijung Kim¹, Jihoon Seo², Sunho Moon², and Ungyu Paik^{1,2}
소속: ¹Department of Nanoscale Semiconductor Engineering, Hanyang University, ²WCU Department of Energy Engineering, Hanyang University
- WL1-J-4 11:50-12:05 Nano-Embossing Ceria Abrasive with Polishing Rate Accelerator for Scratch-Less Poly-Si Stop CMP Application**
저자: Sang-su Yun, Eun-bin Seo, Hao Cui, Jin-Hyung Park, and Jea-Gun Park
소속: Advanced Semiconductor Materials & Device Development Center, Hanyang University