The 21st Korean Conference on Semiconductors
제21회 한국반도체학술대회
February 24-26, 2014 / Hanyang University, Seoul, Korea

N. VLSI CAD 분과

[WJ4-N] Simulation & Testing

Date	Feb. 26, 2014 (Wed.)
Place	Room J / 제1공학관 501호 (# 501, Engineering Building I)

Session Chair: 김윤진 교수(숙명여자대학교), 이종은 교수(UNIST)

WJ4-N-1   17:15-17:30   **HetNoC3D: A User Friendly Simulation Framework for Homogeneous and Heterogeneous 3D NoC Architectures**
저자: Michael Opoku Agyeman and Ali Ahmadinia
소속: School of Engineering and Built Environment, Glasgow Caledonian University

WJ4-N-2   17:30-17:45   **Database 성능 평가를 위한 SQLite Trace 추출 환경**
저자: 이성광, 유승주, 이승구
소속: Department of Electronic Engineering, Pohang University of Science and Technology

WJ4-N-3   17:45-18:00   **Module Regrouping for Minimizing Wrapper Cells in SoC Testing**
저자: 김상민[1], 홍정민[1], 신영수[1], 배상민[2],
소속: [1]KAIST, 전기및전자공학과, [2]LG전자

WJ4-N-4   18:00-18:15   **Accurate Frequency Spectrum Analysis of Event-Driven Simulation Results of Analog/Mixed-Signal Circuits**
저자: Junsuk Kim, Ji-Eun Jang, and Jaeha Kim
소속: Department of Electrical and Computer Engineering, Seoul National University