

The 21<sup>st</sup> Korean Conference on Semiconductors  
**제21회 한국반도체학술대회**  
February 24–26, 2014 / Hanyang University, Seoul, Korea

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L. Analog Design 분과

**[WF4-L] 아날로그 및 혼성 신호 회로 설계 2**

<b>Date</b>	Feb. 26, 2014 (Wed.)
<b>Place</b>	Room F / 제1공학관 404호 (# 404, Engineering Building I)

Session Chair: 안길초 교수(서강대학교), 백광현 교수(중앙대학교)

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- WF4-L-1    17:15-17:30    A Robust DC-DC Converter Protection Scheme for Enhanced PMIC Reliability**  
저자: Yoo-Jun Jeong, Sang-Ik Cho, Hyung-Seok Oh, and Byeong-Ha Park  
소속: Power Device Development Team, System LSI Division, Samsung Electronics Co., Ltd.
- WF4-L-2    17:30-17:45    A Digitally-Controlled, Glitch-Free, 5-GHz Phase Interpolator**  
저자: Chang Soo Yoon, Woorham Bae, and Deog-Kyoon Jeong  
소속: Chang Soo Yoon, Woorham Bae, and Deog-Kyoon Jeong
- WF4-L-3    17:45-18:00    저전력 대 출력 신호 스위치드 캐패시터 앰프 설계**  
저자: 이현의<sup>1</sup>, 최정환<sup>1</sup>, Masaya Miyahara<sup>2</sup>, and Akira Matsuzawa<sup>2</sup>  
소속: <sup>1</sup>삼성전자 메모리사업부, <sup>2</sup>동경공업대학교 이공학연구과
- WF4-L-4    18:00-18:15    A SUC-Based 10 bit 1 GS/s Current Steering DAC in 0.042 mm<sup>2</sup>**  
저자: 김시내, 김미란, 류승탁  
소속: KAIST, 전기 및 전자공학과
- WF4-L-5    18:15-18:30    A Single-Inductor Multiple-Output (SIMO) DC-DC Converter with Wide Operation Voltage Range for Mobile Devices**  
저자: Whan-Seok Seo, Hyun-A Ahn, Young-Ho Jung, Ki-Soo Nam, Jae-Hyung Jung, Seong-Kwan Hong, and Oh-Kyong Kwon  
소속: Department of Electronic Engineering, Hanyang University