

The 21st Korean Conference on Semiconductors
제21회 한국반도체학술대회
February 24–26, 2014 / Hanyang University, Seoul, Korea

O. System LSI Design 분과

[TG2-O] VLSI System Design and Applications II

Date	Feb. 25, 2014 (Tue.)
Place	Room G / 제1공학관 405호 (# 405, Engineering Building I)

Session Chair: 이채은 교수(인하대학교), 장익준 교수(경희대학교)

- TG2-O-1** 11:10-11:25 **Optimized Heterogeneous 3D Networks-on-Chip for High Performance System-on-Chip Design**
저자: Michael Opoku Agyeman and Ali Ahmadinia
소속: School of Engineering and Built Environment, Glasgow Caledonian University
- TG2-O-2** 11:25-11:40 **Fault-Tolerant CGRA-Based Multi-Core Architecture¹**
저자: Seungyun Sohn, Heesun Kim, and Yoonjin Kim
소속: Department of Computer Science, Sookmyung Women's University
- TG2-O-3** 11:40-11:55 **A 256-Radix Crossbar Switch using Mux-Matrix-Mux Folded-Clos Topology**
저자: Sung-Joon Lee and Jaeha Kim
소속: Department of Electrical and Computer Engineering, Seoul National University
- TG2-O-4** 11:55-12:10 **An Efficient Fault-Tolerant Routing Algorithm for 3D Networks-on-Chip**
저자: Michael Opoku Agyeman and Ali Ahmadinia
소속: School of Engineering and Built Environment, Glasgow Caledonian University
- TG2-O-5** 12:10-12:25 **Unified Single-Port Survivor Memory for High-Speed Viterbi Decoder using 3-Unit Packing/Unpacking-Based Data Processing**
저자: Jinil Chung and Jongsun Park
소속: School of Electrical Engineering, Korea University