

The 21st Korean Conference on Semiconductors
제21회 한국반도체학술대회
February 24–26, 2014 / Hanyang University, Seoul, Korea

P. Device for Energy 분과

[TD2-P] Device for Energy II

Date	Feb. 25, 2014 (Tue.)
Place	Room D / 제1공학관 402호 (# 402, Engineering Building I)

Session Chair: 함문호 교수(GIST),

- TD2-P-1 11:10-11:40 Nanostructured Si for Efficient Solar Energy Conversion**
저자: Jihun Oh
소속: Graduate School of EEWS (Energy, Environment, Water and Sustainability), KAIST
- TD2-P-2 11:40-11:55 Effect of Size and Depth of Silicon-Nano-Holes on Surface Reflectance Reduction for {111} Pyramid-Textured Silicon Solar-Cells**
저자: Jae-Hyoung Shim, Seung-Wook Baek, Ji-Heon Kim, Yun-Hyuk Ko, Jin-Seong Park, Gon-Sub Lee and Jea-Gun Park
소속: Department of Electronics and Computer Engineering, Hanyang University
- TD2-P-3 11:55-12:10 Effect of Se&S Composition Ratio on Quantum-Yield, Power-Conversion-Efficiency, Energy-Down-Shifting for CdSe/ZnS Core/Shell Quantum-Dot Implemented Silicon Solar-Cells**
저자: Seung-Wook Baek, Ji-Heon Kim, Jae-Hyoung Shim, Yun-Hyuk Ko, Jin-Seong Park, Gon-Sub Lee, and Jea-Gun Park
소속: Department of Electronics and Computer Engineering, Hanyang University
- TD2-P-4 12:10-12:25 Silicon Solar-Cells Implemented with Energy-Down-Shifting using CdZnS/ZnS Core/Shell Quantum-Dot**
저자: Yun-Hyuk Koh, Seng-Wook Baek, Gon-Sub Lee, and Jea-Gun Park
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