

# 제20회 한국반도체학술대회

2013년 2월 4일(월)~6일(수) / 웰리힐리파크 (구, 성우리조트)

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## D. Thin Film Process Technology 분과

Room E

스타홀 (본관, 2층)

2013년 2월 5일(화) 09:30-10:45

[TE1-D] New channel material technology

좌장: 민요셉(건국대학교), 조영진(삼성종합기술원)

[초청]

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|---------|-------------|--|
| TE1-D-1 | 09:30-10:00 | <b>High-Performance Thin-Film Transistors Based on Multilayer MoS<sub>2</sub> Crystals</b><br>Woong Choi<br>School of Advanced Materials Engineering, Kookmin University   |
| TE1-D-2 | 10:00-10:15 | <b>High Performance Zn-Sn-O Thin Film Transistors with Cu Source/Drain Electrode</b><br>Chul-Kyu Lee <sup>1</sup> , Se-Yeob Park <sup>1</sup> , Hong-Yoon Jung <sup>1</sup> , Chang-kyu Lee <sup>1</sup> ,<br>Byeong-Geun Son <sup>1</sup> , Hyo-Jin Kim <sup>1</sup> , Young-Joo Lee <sup>2</sup> , Young-Chang Joo <sup>2</sup> ,<br>and Jae-Kyeong Jeong <sup>1</sup><br><sup>1</sup> Department of Materials Science and Engineering, Inha University,<br><sup>2</sup> Department of Materials Science and Engineering, Seoul National<br>University |
| TE1-D-3 | 10:15-10:30 | <b>High Performance Amorphous Indium Oxide Thin-Film Transistors with Aqueous Soluble Process</b><br>Kookhyun Choi, Minseok Kim, Seongpil Chang, Shin Woo Jeong,<br>Tae-Yeon Oh, Hyeon Jun Ha, and Byeong-Kwon Ju<br>Display and Nanosystem Laboratory, College of Engineering, Korea<br>University  |
| TE1-D-4 | 10:30-10:45 | <b>Effect of Grain Size and Channel Thickness on Trapping in Polysilicon Thin Film Transistors</b><br>Manh-Cuong Nguyen <sup>1</sup> , Yoon Seok Jeon <sup>1</sup> , Jun Suk Chang <sup>1</sup> , Seung-Won<br>Yoo <sup>1</sup> , Musarrat Hasan <sup>1</sup> , Jae Kyeong Jeong <sup>1</sup> , Bio Kim <sup>2</sup> , Jae-young Ahn <sup>2</sup> ,<br>Kihyun Hwang <sup>2</sup> , and Rino Choi <sup>1</sup><br><sup>1</sup> Inha University, <sup>2</sup> Semiconductor R&D Center, Samsung Electronics Co., Ltd.                                      |