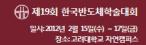
#### **Semiconductor for Smart Living Technologies**

The 19th Korean Conference on Semiconductors



#### D. Thin Film Process Technology 분과

Room F 창의관 (B114)

일 시 : 2월 16일(목) 09:30-11:00 세션명 : [TF1-D] Thin Film Transistor

좌 장: 최창환(한양대학교)

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# TF1-D-1 09:30-10:00 [Invited] Development of Organic Field Effect Transistors for Nonvolatile Flexible Memory

저자: Kang-Jun Baeg and Yong-Young Noh

소속: Department of Chemical Engineering, Hanbat National University

### TF1-D-2 10:00-10:15 Property Analysis of Zinc Tin Oxide Thin Film Grown by Atomic Layer Deposition Process

저자: Un Ki Kim, Yoon Jang Chung, Byoung Keon Park, Eric Hwang, Min Hyuk Park, Taeyong Eom, and Cheol Seong Hwang

소속: WCU Hybrid Materials Program, Department of Materials Science and Engineering and Inter-University Semiconductor Research Center, Seoul National University

### TF1-D-3 10:15-10:30 Amorphous Indium Gallium Zinc Oxide Thin Film Transistors on the Polyethersulfone Substrate Employing the Parylene-C

저자: Seongpil Chang, Jung Ho Park, Shin Woo Jeong, Tae-Yeon Oh, Jong Woo Kim, and Byeong-Kwon Ju

소속: Department of Electrical Engineering, Korea University

#### TF1-D-4 10:30-10:45 Fabrication of Organic Thin Film Transistor using Inkjet-printed Silver Gate Electrode on Substrate with Different Temperature

저자: Jon-Ho Jeun<sup>1</sup>, Jung-Min Kim<sup>1</sup>, Dong-Hoon Lee<sup>1</sup>, and Yong-Sang Kim<sup>1,2</sup>

소속: <sup>1</sup>Department of Nano Science & Engineering, Myongji University, <sup>2</sup>Department of Electrical Engineering, Myongji University

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제19회 한국반도체학술대회 일사:2012년 2월 15일(수) ~ 17일(금) 장소:고려대학교 자연캠퍼스

The 19th Korean Conference on Semiconductors

# TF1-D-5 10:45-11:00 Degradation Mechanism of Poly-Si TFT under Hot-carrier Stress and Positive Bias Stress

저자: Jun-Suk Chang<sup>1</sup>, Tae-Young Jang<sup>1</sup>, Dong-Hyoub Kim<sup>1</sup>, Jung-woo Kim<sup>1</sup>, Musarrat Hasan<sup>1</sup>, Cuong Nguyen Manh<sup>1</sup>, Hoi-chang Yang<sup>1</sup>, Jae-Kyeong Jeong<sup>1</sup>, Bio Kim<sup>2</sup>, Jae-young Ahn<sup>2</sup>, Ki-hyun Hwang<sup>2</sup>, and Rino Choi<sup>1</sup>

소속: <sup>1</sup>Inha University, <sup>2</sup>Process Development P/J2, Semiconductor R&D Center, Samsung Electronics Co., Ltd.