

N. VLSI CAD 분과

Room C
창의관 (116)

일 시 : 2월 17일(금) 15:30-16:45

세션명 : [FC4-N] State-of-the-art Low Power SoC Design Methods

좌 장 : 이종은(울산과학기술대학교), 윤성로(고려대학교)

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- FC4-N-1 15:30-16:00 **[Invited]**How to Implement Low Power SOC under 32nm Process
저자: Jung Yun Choi
소속: Design Technology Team, Samsung Electronics Co., Ltd.
- FC4-N-2 16:00-16:30 **[Invited]**Clock Gating: Design or Synthesis?
저자: Inhak Han and Youngsoo Shin
소속: Department of Electrical Engineering, KAIST
- FC4-N-3 16:30-16:45 Power / Area Optimization by Design Window Reduction in Standard-Cell Based Circuits
저자: Hyung-Ock Kim, Jun Seomun, Jea Han Jeon, Jung Yon Choi, Hyo-Sig Won, and Kee Sup Kim
소속: Samsung Electronics Co., Ltd.