Semiconductor for Smart Living Technologies

## D. Thin Film Process Technology 분과

Room F 창의관 (B114)

## 일 시:2월 17일(금)11:20-12:35

세션명: [FF2-D] Thin Film Deposition

좌 장 : 윤성민(경희대학교), 김형섭(성균관대학교)

FF2-D-1	11:20–11:50	[Invited]Nonvolatile Memory Technology with Engineered Tunnel Barriers Based on High-k Materials 저자: Won-Ju Cho 소속: Department of Electronic Materials Engineering, Kwangwoon University
FF2-D-2	11:50–12:05	Atomic Layer Deposition of (GeTe2)1-x(Sb2Te3)x Film for Phase Change Memory 저자: Taeyong Eom <sup>1</sup> , Seol Choi <sup>1</sup> , Byung Joon Choi <sup>1</sup> , Min Hwan Lee <sup>1</sup> , Taehong Gwon <sup>1</sup> , Sang Ho Rha <sup>1</sup> , Woongkyu Lee <sup>1</sup> , Moo-Sung Kim <sup>2</sup> , Manchao Xiao <sup>3</sup> , and Cheol Seong Hwang <sup>1</sup> 소속: <sup>1</sup> WCU Hybrid Material Program, Department of Materials Science and Engineering, Seoul National University, and Inter- university Semiconductor Research Center, Seoul National University, <sup>2</sup> Air Products Korea, <sup>3</sup> Air Products and Chemicals, Inc.
FF2-D-3	12:05–12:20	Atomic Layer Deposition of SrTiO3 Films with Cp-based Precursors 저자: Woongkyu Lee, Jeong Hwan Han, Woojin Jeon, and Cheol Seong Hwang 소속: WCU Hybrid Materials Program, Department of Materials Science and Engineering and Inter-university Semiconductor Research Center, Seoul National University
FF2-D-4	12:20–12:35	Investigation on Suppression of Nickel-Silicide Formation by Fluorocarbon Reactive Ion Etch (RIE) and Plasma-Enhanced Deposition 저자: Hyun Woo Kim <sup>1</sup> , Jung Han Lee <sup>1</sup> , Min-Chul Sun <sup>1,2</sup> , and Byung- Gook Park <sup>1</sup> 소속: <sup>1</sup> Inter-University Semiconductor Research Center (ISRC) and

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School of Electrical Engineering and Computer Science, Seoul National University, <sup>2</sup>TD Team (S. LSI), Semiconductor Business Group, Samsung Electronics Co., Ltd.

## FF2-D-5 12:35-12:50 Comparative Study of Ultralow-k pSiCOH (k=2.5) Films by using Different C-bridged Si-precursors

- 저자: Gyeonghee Kim<sup>1</sup>, Sang Hoon Ahn<sup>1</sup>, Insun Jung<sup>2</sup>, Kyu-Hee Han<sup>1</sup>, Janghee Lee<sup>1</sup>, Jongho Yun<sup>1</sup>, Gil Heyun Choi<sup>1</sup>, Ho Kyu Kang<sup>1</sup>, and Chillhee Chung<sup>1</sup>
- 소속: <sup>1</sup>Process Development Team, Semiconductor R&D Center, Samsung Electronics Co., Ltd., <sup>2</sup>Analytical Engineering Group, AE Center, Samsung Advanced Institute of Technology