

## O. System LSI Design 분과

Room C

창의관 (116)

일 시 : 2월 17일(금) 11:20-12:35

세션명 : [FC2-O] Digital VLSI Circuits and Systems

좌 장 : 조준호(서울 벨연구소), 이한호(인하대학교)

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FC2-O-1 11:20-11:35 An Efficient Overlapped LDPC Decoder with a Dual-diagonal Structure  
저자: Yong Ki Byun, Jong Kang Park, Soongyu Kwon, and Jong Tae Kim  
소속: School of Information and Communication Engineering, Sungkyunkwan University

FC2-O-2 11:35-12:05 **[Invited]** Design of Convolutional Single Parity-check Codes with Structure  
저자: 조준호  
소속: 서울 벨 연구소

FC2-O-3 12:05-12:20 Low Power CORDIC Architecture using Trigonometric Characteristics  
저자: 이민우, 박종선  
소속: 고려대학교 전기전자전파공학과

FC2-O-4 12:20-12:35 Low Power Multiplexer at Data Rate of 8 Gb/s with PMOS Latch  
저자: Yifei Li, Suki Kim, and Sang-Hyuk Yang  
소속: Department of Electrical Engineering, Korea University