

## Room I

### 하나스퀘어 (아뜨리움)

일 시 : 2월 17일(금) 09:30-12:35

세션명 : [FP1] Poster I

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#### I. MEMS & Sensors 분과

- FP1-1 09:30-12:35 Novel Biosensor based on MOSFET-BJT Hybrid Mode of Gated Lateral BJT for C-reactive Protein Detection  
저자: H. Yuan, B. Wang, S. H. Yeom, H. C. Kwon, and S. W. Kang  
소속: School of Electrical Engineering and Computer Science,  
Kyungpook National University
- FP1-2 09:30-12:35 Electrical Properties of Silicon Nanowire Integrated Highly Sensitive Electrolyte-insulator-semiconductor (EIS) Bio-chemical Sensor  
저자: Jinyong Oh<sup>1</sup>, M. Saif Islam<sup>1</sup>, Hyun-June Jang<sup>2</sup>, Tae-On Bae<sup>2</sup>,  
and Won-Ju Cho<sup>2</sup>  
소속: <sup>1</sup>University of California Davis, <sup>2</sup>Kwangwoon University
- FP1-3 09:30-12:35 Single Photon Detection for Quantum Cryptography Applications  
저자: A. Bouzid, J. B. Park, and S. Moon  
소속: Nanophotonics Research Center, Korea Institute of Science  
and Technology
- FP1-4 09:30-12:35 MEMS 공정을 이용한 이중빔 PZT 외팔보 에너지수확소자의 제작 및  
특성  
저자: 김문근<sup>1,2</sup>, 황범석<sup>1</sup>, 정재화<sup>1</sup>, 민남기<sup>1</sup>, 이상균<sup>2</sup>, 양일석<sup>2</sup>, 권광호<sup>1</sup>  
소속: <sup>1</sup>고려대학교, <sup>2</sup>한국전자통신연구원
- FP1-5 09:30-12:35 A RF MEMS Tunable Capacitor with Large Tuning Range using  
Aluminum Nitride Film and Two Air Gap Structure  
저자: W. J. Jang, S. J. Cheon, and J. Y. Park  
소속: Micro/Nano Devices and Packaging Lab. Department of  
Electronic Engineering, Kwangwoon University

## D. Thin Film Process Technology 분과

- FP1-6 09:30-12:35 **Growth of Conductive SrRuO<sub>x</sub> Films by Combined CVD/ALD Process**  
저자: Jeong Hwan Han, Woongkyu Lee, Woojin Jeon, and Cheol Seong Hwang  
소속: WCU Hybrid Materials Program, Department of Materials Science and Engineering and Inter-university Semiconductor Research Center, Seoul National University
- FP1-7 09:30-12:35 **Resistive Switching Characteristics in HfO<sub>2</sub> Thin Films Depending on the Crystalline Structure**  
저자: 윤정호, 정형석, 이민환, 김건환, 송슬지, 석준영, 윤경진, 황철성  
소속: 서울대학교 재료공학부 유전박막연구실
- FP1-8 09:30-12:35 **The Advanced Characteristics of Thermal CVD Silicon Oxide in a Single-wafer Chamber**  
저자: 이웅, 김용석, 신현진, 이우성, 임현형, 황기현, 신유균  
소속: Semiconductor R&D Center, Samsung Electronics Co., Ltd.
- FP1-9 09:30-12:35 **Development of High Performance and High Stability Transistors without Junctions**  
저자: 정승민<sup>1</sup>, 오진용<sup>2</sup>, M. Saif Islam<sup>2</sup>, 조원주<sup>1</sup>  
소속: <sup>1</sup>광운대학교, 전자재료공학과, <sup>2</sup>Department of Electrical and Computer Engineering, University of California Davis
- FP1-10 09:30-12:35 **저온 스퍼터증착법을 이용한 플렉서블 적층 저항변화 메모리의 제작 및 특성 연구**  
저자: 한용<sup>1</sup>, 조경아<sup>2</sup>, 박석형<sup>2</sup>, 김상식<sup>1,2</sup>  
소속: <sup>1</sup>고려대학교 나노반도체공학과,  
<sup>2</sup>고려대학교 전기전자전파공학과
- FP1-11 09:30-12:35 **Mn doped ZnO<sub>x</sub>S1-x 저항변화 메모리소자 특성에 미치는 전극물질의 영향**  
저자: 한용<sup>1</sup>, 조경아<sup>2</sup>, 윤정권<sup>2</sup>, 김상식<sup>1,2</sup>  
소속: <sup>1</sup>고려대학교 나노반도체공학과,  
<sup>2</sup>고려대학교 전기전자전파공학과

- FP1-12 09:30~12:35**      **Effect of Oxygen Plasma Annealing on Electrical Properties of Sol-gel Processed ZrO<sub>2</sub> Films**  
저자: Dong-Hyoub Kim, Musarrat Hasan, Tae-Young Jang, Jungwoo Kim, Jun Suk Chang, Manh Cuong Nguyen, and Rino Choi  
소속: Inha University
- FP1-13 09:30~12:35**      **Dipole-induced Conduction Process Change in La-incorporated Hafnium-based Dielectric**  
저자: Tae-Young Jang, Dong-Hyoub Kim, Jungwoo Kim, Jun Suk Chang, Cuong Nguyen Manh, Musarrat Hasan, and Rino Choi  
소속: Department of Materials Science and Engineering, Inha University
- FP1-14 09:30~12:35**      **Impacts of Ar/N<sub>2</sub> Flow rates of Sputtered TiN Metal Gate on Electrical Properties in Gate-first Processed MOS Devices**  
저자: Dongjun Yoo, Seung-Chan Heo, and Changhwan Choi  
소속: Division of Materials Science and Engineering, Hanyang University
- FP1-15 09:30~12:35**      **Low-temperature Atomic Layer Deposition of Cobalt Oxide Thin Films using Cyclopentadienylcobalt Dicarbonyl and Ozone**  
저자: 최규하<sup>1</sup>, 한별<sup>1</sup>, 박정우<sup>2</sup>, 이원준<sup>1</sup>  
소속: <sup>1</sup>세종대학교 나노신소재공학과, <sup>2</sup>한솔케미칼 박막재료팀
- FP1-16 09:30~12:35**      **Growth of Zn-Sn-O Films using by Plasma Enhanced Atomic Layer Deposition for TFTs Applications**  
저자: B. K. Lee<sup>1,2</sup>, D. C. Moon<sup>1</sup>, E.-A. Jung<sup>1</sup>, S. S. Lee<sup>1</sup>, B. K. Park<sup>1</sup>, J. H. Hwang<sup>2</sup>, T.-M. Chung<sup>1</sup>, C. G. Kim<sup>1</sup>, and K. S. An<sup>1</sup>  
소속: <sup>1</sup>Thin Film Materials Research Team, Korea Research Institute of Chemical Technology, <sup>2</sup>Department of Material Science and Engineering, Hongik University
- FP1-17 09:30~12:35**      **ZnO Nano-wire Deposited by Metal Organic Chemical Vapor Deposition (MOCVD) for Anti Reflection Coating (ARC) of Si Solar Cell**  
저자: 최은석<sup>1</sup>, 장삼석<sup>1</sup>, 임소영<sup>2</sup>, 탁성주<sup>1</sup>, 김동환<sup>1</sup>, 변동진<sup>1</sup>  
소속: <sup>1</sup>Department of Materials Science and Engineering, Korea

University, <sup>2</sup>Department of Nano Semiconductor Engineering,  
Korea University

- FP1-18 09:30-12:35 **Influence of Argon Neutral Particle Beam of High Energy in the Neutral Particle Beam Sputtering System Assisted the Change of Structural Properties on the Amorphous Carbon Film**  
저자: DongHyeok Lee<sup>1</sup>, JinNyoung Jang<sup>1</sup>, KwangHo Kwon<sup>2</sup>, SukJae You<sup>3</sup>, BonJu Lee<sup>3</sup>, and MunPyo Hong<sup>1</sup>  
소속: <sup>1</sup>Department of Display and Semiconductor Physics, Korea University, <sup>2</sup>Department of Control and Instrumentation Engineering, Korea University, <sup>3</sup>National Fusion Research Institute

## J. Nano-Science & Technology 분과

- FP1-19 09:30-12:35 **Fabrication of Silicon Nanowire Based Thermoelectric Device and Temperature Sensor Calibration**  
저자: Wonchul Choi<sup>1,2</sup>, Youngsam Park<sup>1</sup>, Younghoon Hyun<sup>1</sup>, Taehyoung Zyung<sup>1</sup>, Jaehyun. Kim<sup>1,3</sup>, Mincheol Shin<sup>2</sup>, and Moongyu Jang<sup>1,3</sup>  
소속: <sup>1</sup>Convergence Components & Material Research Lab., Electronics and Telecommunications Research Institute, <sup>2</sup>Department of Electrical Engineering, KAIST, <sup>3</sup>Department of Advanced Device Technology, UST

- FP1-20 09:30-12:35 **Fabrication of Sub-30nm Pillar Array by Oxygen Plasma Treatment**  
저자: Bongho Kim<sup>1</sup>, Daehong Kim<sup>1</sup>, Jihun Kwon<sup>1</sup>, Sungwoo Chun<sup>1</sup>, Seonjun Choi<sup>1</sup>, and Seung-Beck Lee<sup>1,2,3</sup>  
소속: <sup>1</sup>Department of Electronic Engineering, Hanyang University, <sup>2</sup>Department of Nanoscale Semiconductor Engineering, Hanyang University, <sup>3</sup>Institute of Nano Science and Technology, Hanyang University

- FP1-21 09:30-12:35 **Thin Film Fabrication and Simultaneous Reduction of Deposited Graphene Oxide Platelets by Electrophoretic Deposition**  
저자: Sung Jin An  
소속: School of Advanced Materials and Systems Engineering, Kumoh National Institute of Technology

- FP1-22 09:30~12:35     Ion-gel Gate Dielectrics for Arrayed Si Nanowires Field Effect Transistors  
저자: 최진용, 조경아, 김상식  
소속: 고려대학교 전기전자전파공학과
- FP1-23 09:30~12:35     Fabrication of Beta-phase Poly(9,9-diptylfluorene) Nanowire Array using Direct Printing Method  
저자: Jangmi Back and Myung M. Sung  
소속: Department of Chemistry, Hanyang University
- FP1-24 09:30~12:35     Graphene Sheets as P-type Transparent Conducting Electrodes in GaN Light Emitting Diodes  
저자: Jung Min Lee, Hae Yong Jeong, and Won Il Park  
소속: Division of Materials Science and Engineering, Hanyang University
- FP1-25 09:30~12:35     Fabrication of Vapor Phase Polymerized PEDOT Nanowire Arrays using Liquid-bridge-mediated Nanotransfer Molding  
저자: Boram Cho, Hyun S. Oh, and Myung M. Sung  
소속: Department of Chemistry, Hanyang University
- FP1-26 09:30~12:35     N-type Carbon Nanotube Network Device Based on Tunneling through SnO<sub>2</sub>  
저자: Young Jun Heo, Jun Ho Cheon, Seok Ha Lee, Jaeheung Lim, and Young June Park  
소속: School of Electrical Engineering, Seoul National University
- FP1-27 09:30~12:35     The Predicted Crystal Structure of Li<sub>4</sub>C<sub>6</sub>O<sub>6</sub>, an Organic Cathode Material for Li-ion Batteries: First-principles Multi-scale Computational Study  
저자: Dong-Hwa Seo<sup>1</sup>, Hyungjun Kim<sup>2</sup>, Haegyeom Kim<sup>1</sup>, William A. Goddard III<sup>2,3</sup>, and Kisuk Kang<sup>1</sup>  
소속: <sup>1</sup>Department of Materials Science and Engineering, Seoul National University, <sup>2</sup>Graduate School of EEE, KAIST, <sup>3</sup>Materials and Process Simulation Center, California Institute of Technology

- FP1-28 09:30~12:35     **Fabricated Various Metallic Nano-sized Pattern using Ag Ink Printing Technique**

저자: 오상철<sup>1</sup>, 신주현<sup>2</sup>, 김진승<sup>2</sup>, 김양두<sup>3</sup>, 이 헌<sup>1,2,3</sup>

소속: <sup>1</sup>고려대학교 나노반도체공학과, <sup>2</sup>고려대학교 신소재공학과, <sup>3</sup>고려대학교 바이오-마이크로 시스템 협동과정

- FP1-29 09:30~12:35     **A Graphene/Nanocluster Hybrid Nanomaterial-based Gas Sensor**

저자: I.-S. Kang and C. W. Ahn

소속: National Nanofab Center, Korea Advanced Institute of Science and Technology

- FP1-30 09:30~12:35     **Color Tunable OLEDs using Localized Surface Plasmons**

저자: Illhwon Lee, Kihyon Hong, Sungjun Kim, and Jong-Lam Lee

소속: Department of Materials Science and Engineering and Division of Advanced Materials Science, Pohang University of Science and Technology

- FP1-31 09:30~12:35     **Analytic Model of Spin-Torque Oscillators(STO) for Circuit-level Simulation**

저자: 안소라<sup>1</sup>, 임혜인<sup>1</sup>, 서수만<sup>2</sup>, 이경진<sup>2</sup>, 신형순<sup>1</sup>, 이승준<sup>1</sup>

소속: <sup>1</sup>이화여자대학교 전자공학과, <sup>2</sup>고려대학교 신소재공학과

- FP1-32 09:30~12:35     **Porosity Modulated Silicon Nanowires**

저자: Jungkil Kim<sup>1,2</sup>, and Woo Lee<sup>1,2</sup>

소속: <sup>1</sup>Korea Research Institute of Standards and Science,

<sup>2</sup>Department of Nano Science, University of Science and Technology

## N. VLSI CAD 분과

- FP1-33 09:30~12:35     **Thermal Modeling of 3D Stacked MLC NAND Flash Memory**

저자: 김동기, 유승주, 이승구

소속: 포항공과대학교 전자전기공학과

- FP1-34 09:30~12:35     **System Model for CPU/GPU Architecture**

저자: 이성광<sup>1</sup>, 유승주<sup>1</sup>, 정재웅<sup>2</sup>, 우동혁<sup>2</sup>, 김대현<sup>2</sup>

소속: <sup>1</sup>포항공과대학교 전자전기공학과, <sup>2</sup>Intel Corporation

- FP1-35 09:30-12:35     High-throughput Double-binary MAP Decoder with Reduced Memory Requirement  
저자: 김지훈  
소속: 충남대학교 전자공학과

- FP1-36 09:30-12:35     Promoting Data Reuse on Shared Memory of Hybrid System  
저자: Toan X. Mai, YeonghunJeong, and Jongeun Lee  
소속: School of Electrical and Computer Engineering, Ulsan National Institute of Science and Technology

- FP1-37 09:30-12:35     쓰기 데이터의 특성 파악을 통한 Wear-leveling  
저자: 유태희, 박상훈, 서혁준, 정의영  
소속: 연세대학교 전기전자공학과

- FP1-38 09:30-12:35     Interleaved Garbage Collection Scheme using Dynamic Channel/Way Allocation for Solid-state Drive  
저자: 김동건, 박상훈, 서혁준, 정의영  
소속: 연세대학교 전기전자공학과

## Q. Metrology, Inspection, and Yield Enhancement 분과

- FP1-39 09:30-12:35     초음속 나노입자빔을 이용한 실리콘 웨이퍼 표면오염입자(10nm) 제거 실험  
저자: 김인호, 이진원  
소속: 포항공과대학교 기계공학과

- FP1-40 09:30-12:35     Characterization of Overlay Error Induced by Film Stress using Local Stress Monitoring Tool  
저자: C. H. Lee<sup>1</sup>, J. T. Kim<sup>1</sup>, J. H. Kim<sup>1</sup>, H. Y. Yoo<sup>1</sup>, I. K. Han<sup>1</sup>, and W. S. Yoo<sup>2</sup>  
소속: <sup>1</sup>Hynix Semiconductor Inc., <sup>2</sup>WaferMasters, Inc.

- FP1-41 09:30-12:35     The Pattern Wiggling CD Metrology using Flexible Scan & Dense Function

저자: 김종태<sup>1</sup>, 이창환<sup>1</sup>, 유형원<sup>1</sup>, 한일근<sup>1</sup>, 고경보<sup>2</sup>, 곽동수<sup>3</sup>  
소속: <sup>1</sup>하이닉스 반도체 MI팀, <sup>2</sup>하이닉스 반도체 DRAM공정AP팀,  
<sup>3</sup>Hitachi High-Technologies Corporation

**FP1-42 09:30-12:35 Electron Beam Inspection of Cell Mat Edge using Image Averaging and Comparison Metho**

저자: G. Kwon<sup>1</sup>, J. H. Oh<sup>1</sup>, D. Y. Mun<sup>1</sup>, J. C. Jo<sup>2</sup>, J. S. Koo<sup>2</sup>, M. Nozoe<sup>2</sup>, T. Ninomiya<sup>2</sup>, T. Hiroi<sup>2</sup>, H. Okuda<sup>2</sup>, and H. W. Yoo<sup>1</sup>  
소속: <sup>1</sup>Hynix Semiconductor Inc.,  
<sup>2</sup>Central Research Laboratory, Hitachi High-Technologies Corporation

**FP1-43 09:30-12:35 The Measurement of OCD (Optical critical dimension) for Yield Enhancement at Edge Die**

저자: Seok Park, Won Sik Yun, Chang Hwan Lee, Sung Su Kim, Hyung Won Yoo, and Il Keoun Han  
소속: Hynix Semiconductor Inc.

**FP1-44 09:30-12:35 The MASK CD Control (CDC) using OCD applications**

저자: Hyun Chul Shin, Seok Park, Won Sik Yun, Chang Hwan Lee, Hyung Won Yoo, and Il Keoun Han  
소속: Hynix Semiconductor Inc.

**FP1-45 09:30-12:35 The In-Line Monitoring Method of ZAZ Thickness Using WD-XRF**

저자: Jun Soo Kim, Sang Hoon Son, Shin Wang Ju, Hyung Won Yoo, and Il Keoun Han  
소속: Hynix Semiconductor Inc.

**FP1-46 09:30-12:35 Substrate Materials Dependent Growth Characteristics of the Initial Stage of ALD Ruthenium Films using Synchrotron Radiation X-ray Scattering**

저자: Y. J. Park<sup>1</sup> and D. R. Lee<sup>2</sup>  
소속: <sup>1</sup>Pohang Accelerator Laboratory, Pohang University of Science and Technology, <sup>2</sup>Department of Physics, Soongsil University

**FP1-47 09:30-12:35 Improvement of Measurement Resolution for Determining Geometrical Thickness of a Silicon Wafer using a Femtosecond Pulse Laser**

저자: 맹세름<sup>1,3</sup>, 박정재<sup>1</sup>, 진종한<sup>1,2</sup>, 김재완<sup>1,2</sup>, 강주식<sup>1,2</sup>, 김종안<sup>1</sup>, 오병성<sup>3</sup>

# Semiconductor for Smart Living Technologies

The 19<sup>th</sup> Korean Conference on Semiconductors

제19회 한국반도체학술대회

일시: 2012년 2월 15일(수) ~ 17일(금)

장소: 고려대학교 자연캠퍼스

소속: <sup>1</sup>한국표준과학연구원 길이센터, <sup>2</sup>과학기술연합대학원대학교,  
<sup>3</sup>충남대학교 물리학과

FP1-48 09:30-12:35

다결정성팅스텐 나노팁의 저전압 전계방출 안정성 평가

저자: 정재은, 안상정, 박창준, 김주황, 송운, 김달현, 배문섭, 이학주,  
조양구

소속: 한국표준과학연구원 나노이미징기술센터